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8 9	UNITED STATES DIS WESTERN DISTRICT O AT SEATT	STRICT COURT OF WASHINGTON LE			
10 11	POLARIS POWERLED TECHNOLOGIES, LLC,	No.			
12	Plaintiff,	COMPLAINT FOR PATENT			
13	v.				
14	NINTENDO CO., LTD., and NINTENDO OF AMERICA, INC.,	JURY DEMAND			
15 16	Defendants.				
17	Plaintiff Polaris PowerLED Technologie	es, LLC ("Polaris PowerLED"), by and			
18	through its undersigned counsel, files this Compla	aint for Patent Infringement relating to U.S.			
19	Patent No. 8,223,117 ("'117 Patent") and alleges	as follows:			
20	THE PART	<u>ries</u>			
21	1. Plaintiff Polaris PowerLED Tech	nnologies, LLC ("Polaris PowerLED" or			
22	"Plaintiff") is a California limited liability company, with its address at 32932 Pacific Coast				
23	Highway #14-498, Dana Point, California, 92629.				
24	2. Upon information and belief, Defendant Nintendo Co., Ltd. is Japanese				
25	company with a regular place of business at 1101 Kamitoba hokotate-cho, Minami-ku, Kyoto				
26	601-8501 Japan.				
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3. Upon information and belief, Defendant Nintendo of America, Inc. is a Washington corporation, with its principal place of business at 4600 150th Avenue NE, Redmond, Washington 98052 and has a registered agent for service of process at CT Corporation System, 711 Capitol Way S, Suite 204, Olympia, Washington 98501.

4. Collectively, Nintendo of America, Inc. and Nintendo Co., Ltd. shall be referred to as "Defendants" or "Nintendo."

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JURISDICTION AND VENUE

5. Polaris PowerLED brings this civil action for patent infringement pursuant to the Patent Laws of the United States, 35 U.S.C. § 1, *et seq*. This Court has subject matter jurisdiction over this action pursuant to 28 U.S.C. §§ 1331 and 1338(a).

6. This Court has both general and specific jurisdiction over Nintendo because Nintendo has committed acts within this District giving rise to this action and has established minimum contacts with this forum such that the exercise of jurisdiction over Nintendo would not offend traditional notions of fair play and substantial justice. Nintendo, directly and through subsidiaries, intermediaries (including distributors, retailers, franchisees and others), has committed and continues to commit acts of patent infringement in this District, by, among other things, making, using, selling, offering for sale, and/or importing products that infringe the '117 Patent.

7. Venue is proper in this District under 28 U.S.C. §§ 1391(b)-(d) and 1400(b) because Nintendo has committed acts of infringement in this District and has a regular and established place of business in this District. Nintendo employs numerous people in this district and has a principal place of business at 4600 150th Avenue NE, Redmond, Washington 98052. Nintendo has committed acts of infringement in this District, directly and/or through intermediaries, by, among other things, making, using, offering to sell, selling, and/or importing products that infringe the '117 Patent, as alleged herein. Nintendo has additionally committed acts of infringement in this District, by, among other

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things, inducing others to infringe the '117 Patent, as alleged herein. Intradistrict assignment in the Seattle Division of this District is appropriate under LCR 3(e) because a substantial part of the events that give rise to the claim occurred in King County.

THE PATENT-IN-SUIT

8. Polaris PowerLED owns by the entire right, title, and interest in the '117 Patent, which is titled "Method and Apparatus to Control Display Brightness with Ambient Light Correction." The '117 Patent issued on July 17, 2012 to inventor Bruce R. Ferguson from the U.S. Patent Application No. 12/336,990, filed on December 17, 2008. A true and correct copy of the '117 Patent is attached as Exhibit A to this Complaint.

9. Bruce Ferguson invented an important and novel manner of adjusting the brightness of a display screen in response to ambient light, conserving power, reducing eye strain, and significantly improving the experience of the user. His inventions were a significant advance in the field of display technology, power conservation and power control for electronics products, including televisions and other devices. Mr. Ferguson patented these innovations in the '117 Patent.

- 10. Claim 1 of the '117 Patent, for example, reads as follows:
 - 1. A brightness control circuit with selective ambient light correction comprising:
 - a first input configured to receive a user signal indicative of a user selectable brightness setting;
 - a light sensor configured to sense ambient light and to output a sensing signal indicative of the ambient light level;
 - a multiplier configured to selectively generate a combined signal based on both the user signal and the sensing signal; and
 - a dark level bias configured to adjust the combined signal to generate a brightness control signal that is used to control a brightness level of a visible display such that the brightness control signal is maintained above a predetermined level when the ambient light level decreases to approximately zero.



COUNT I

(INFRINGEMENT OF U.S. PATENT NO. 8,223,117)

11.

Polaris PowerLED incorporates by reference paragraphs 1-10 above.

12. Nintendo has directly and indirectly infringed, and continues to directly and indirectly infringe, one or more claims of the '117 Patent, including at least claim 1 of the '117 Patent, literally and/or under the doctrine of equivalents. Nintendo has directly infringed, and continues to directly infringe one or more claims of the '117 Patent, including at least claim 1 of the '117 Patent, literally and/or under the doctrine of equivalents, by or through making, using, offering for sale, selling within the United States, and/or importing into the United States video game devices (the "Accused Products"), including, for example, the Nintendo Switch products that contain ambient light sensors and autobrightness control features in violation of 35 U.S.C. § 271(a).

13. The Accused Products include "a brightness control circuit with selective ambient light correction" as required by claim 1. For example, the Nintendo Switch products contain a brightness control circuit with hardware components and/or software that detects ambient light and adjusts the brightness based on the ambient light level. For example, the Nintendo Switch products contain at least an ambient light sensor that detects ambient light as well as a processor and software. Ex. B, Technical Specs of Nintendo Switch, *available at* https://www.nintendo.com/switch/tech-specs/. The brightness control circuit implements selective ambient light correction, such as, for example, in the autobrightness control feature based on the settings of the Accused Products.

14. As a representative example, the Nintendo Switch products include an ambient light sensor and software to allow the device to selectively control display brightness based on ambient light levels.

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Speaker Ambient Light Speaker

15. This autobrightness adjustment feature can be adjusted by moving the brightness slider bar, as shown in the below photo of the brightness adjustment screen and separately in the online user manual. Thus, the Nintendo Switch products include a brightness control circuit with selective ambient light correction.





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	Note
2	Screen brightness can be adjusted only while in bandheld mode or tableton
5	mode.
	 When Auto-Brightness is enabled, the screen brightness will adjust based on ambient conditions.
<i>,</i>	Screen brightness can be adjusted in the following ways:
;	From the Quick Settings Menu.
,	 Press and hold down the HOME Button for at least one second to open the Quick Settings Menu.
;	2. Enable/disable Auto-Brightness, or manually adjust the screen brightness by sliding
)	
,	From Within System Settings.
	From the HOME Menu, select System Settings , then Screen Brightness , then Auto-Brightness Adjustment , and finally select On or manually adjust the screen brightness by
	sliding the brightness level bar.
<u>'</u>	System Settings
;	Support/Health & Safety Auto-Brightness Adjustment ON
Ļ	Airplane Mode
;	Screen Brightness This can also be changed from Quick Settings (hold @).
;	Screen Lock
	Parental Controls
	Internet
;	Data Management
,	The Auto-Brightness Adjustment setting being displayed in the Screen Brightness menu within the
,	Nintendo Switch System Settings
′	

Ex. C, Nintendo Switch online manual, *available at* https://en-americassupport.nintendo.com/app/answers/detail/a_id/22330/p/989/c/990#DT:t1-q1a1EP:t1-q1a1-c.

16. The Accused Products include "a first input configured to receive a user signal indicative of a user selectable brightness setting" as required by claim 1 of the '117 Patent. The Accused Products include, for example, a brightness bar providing a user selectable brightness setting based on the position of the slider bar. As a result of the user moving the

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slider bar, the system, in hardware and/or software, generates a user signal indicative of the user selectable brightness setting. The Accused Products include hardware and/or software with a first input that is configured to receive the user signal.

17. For example, a user may use the brightness slider bar on the Nintendo Switch products to adjust the screen brightness, which is a user selectable brightness setting:





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1	
	Note
2	Screen brightness can be adjusted only while in handheld mode or tabletop mode
3	 When Auto-Brightness is enabled, the screen brightness will adjust based on
4	ambient conditions.
5	
6	Screen brightness can be adjusted in the following ways:
7	From the Quick Settings Menu.
Q	1. Press and hold down the <u>HOME Button</u> for at least one second to open the <u>Quick</u> Settings Menu.
9	 Enable/disable Auto-Brightness, or manually adjust the screen brightness by sliding the brightness level bar.
10	From Within System Settings.
11	From the HOME Menu, select System Settings , then Screen Brightness , then Auto-
12	sliding the brightness level bar.
13	System Settings
14	Support/Health & Safety Auto-Brightness Adjustment ON
15	Airplane Mode
16	Screen Brightness This can also be changed from Quick Settings (hold (®)).
17	Screen Lock
17	Parental Controls
18	Data Management
19	Back OK
20	The Auto-Brightness Adjustment setting being displayed in the Screen Brightness menu within the Nintendo Switch System Settings
21	
22	Ex. C, Nintendo Switch online manual, available at https://en-americas-
23	support.nintendo.com/app/answers/detail/a_id/22330/p/989/c/990#DT:t1-q1a1EP:t1-q1a1-c.
24	18. The Accused Products include "a light sensor configured to sense ambient
25	light and to output a sensing signal indicative of the ambient light level" as required by claim

1 of the '117 Patent, as shown below in the Nintendo Switch products and in the Technical



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outputs a sensing signal indicative of the ambient light.

19. The Accused Products include "a multiplier configured to selectively generate a combined signal based on both the user signal and the sensing signal" as required by claim 1 of the '117 Patent. The Accused Products, including the Nintendo Switch products, have a multiplier in hardware and/or software that is configured to generate a combined signal based on both the user signal and the sensing signal. The multiplier selectively generates the combined signal depending on the configured settings.

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20. The Accused Products, like the Nintendo Switch products, include "a dark

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level bias configured to adjust the combined signal to generate a brightness control signal that is used to control a brightness level of a visible display such that the brightness control signal is maintained above a predetermined level when the ambient light level decreases to approximately zero" as required by claim 1 of the '117 Patent. The Accused Products include, for example, hardware and/or software that includes a dark level bias configured to adjust the combined signal.

21. The dark level bias is stored in hardware or as a software variable and is used to adjust the combined signal to generate a brightness control signal that is used to control a brightness level of a visible display such that the brightness control signal is maintained above a predetermined level when the ambient light level decreases to approximately zero. The dark level bias is used to adjust the combined signal to generate a brightness control signal in the Accused Products. When the ambient light level decreases to approximately zero, the dark level bias is used such that the brightness control signal is maintained above a predetermined level. This can prevent the display from becoming unviewable in very low ambient light conditions.

22. Nintendo has indirectly infringed and continues to indirectly infringe the '117 Patent by inducing infringement of the '117 Patent by its customers, users, and third parties. Nintendo received notice of the '117 Patent and of its infringement of the '117 Patent by no later than February 11, 2020 by virtue of a letter from Polaris PowerLED to Nintendo. From at least the time Nintendo received notice, Nintendo has been actively inducing its customers, users, and third parties to infringe at least claim 1 of the '117 Patent.

23. Nintendo has taken affirmative actions to induce infringement by intentionally instructing its customers, users, and third parties to directly infringe one or more claims of the '117 Patent, including at least claim 1, through instructions, training videos, demonstrations, brochures and user guides that instruct on the infringing use and implementation of the automatic brightness functionality, such as those in Ex. C, Nintendo

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Switchonlinemanual,availableathttps://en-americas-support.nintendo.com/app/answers/detail/a_id/22330/p/989/c/990#DT:t1-q1a1EP:t1-q1a1-c; Ex. D, available at https://www.nintendo.com/sg/support/qa/detail/34040; and Ex. B,Technical Specs of Nintendo Switch, available at https://www.nintendo.com/switch/tech-specs/.

24. Nintendo has specifically intended, and still intends, that its customers, end users, and third parties infringe the '117 Patent. Nintendo has been, and still is, aware that the acts of its customers, end users, and third parties in making and using the autobrightness and ambient light sensor features of the Accused Products infringe one or more claims of the '117 patent, including at least claim 1. Nintendo has known and intended that its continued actions would actively induce the direct infringement of one or more claims of the '117 Patent, including at least claim 1, by its customers, end users, and third parties.

25. With knowledge of both the '117 Patent and its infringement of the '117 Patent, Nintendo has acted with specific intent or willful blindness to actively aid and abet its customers, end users, and third parties in infringing the '117 Patent by making and using the autobrightness and ambient light sensor features of the Accused Products in a manner constituting direct infringement of at least claim 1 of the '117 Patent. Nintendo is thus liable for infringement of the '117 Patent under 35 U.S.C. § 271(b).

26. Nintendo has willfully infringed, and continues to willfully infringe, the '117 Patent. By at least as early as February 11, 2020, when Polaris sent Nintendo a letter regarding the '117 Patent, Nintendo had actual knowledge of the '117 Patent and knowledge that its activities were infringing the '117 Patent. After receiving actual knowledge of the '117 Patent and of its infringement of the '117 Patent, Nintendo willfully continued to make, use, sell, offer for sale, and/or import into the United States infringing products despite knowing that there was a high likelihood of infringement and, in fact, being on notice of such infringement. Additionally, after receiving actual knowledge of the '117 Patent and of its

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infringement of the '117 Patent, Nintendo willfully continued to induce infringement by its customers, end users, and third parties. Nintendo further actively promotes the infringing autobrightness control and ambient light sensor features in its products.

27. On information and belief, Nintendo's past and continuing infringement has been deliberate and willful, and this case is therefore an exceptional case, which warrants award of treble damages and attorneys' fees to Plaintiff pursuant to 35 U.S.C. § 285.

28. As a result of Nintendo's infringement of the '117 Patent, Polaris PowerLED has suffered monetary damages and is entitled to no less than a reasonable royalty for Nintendo's use of the claimed inventions of the '117 Patent, together with interest and costs as determined by the Court. Polaris PowerLED will continue to suffer damages in the future unless Nintendo's infringing activities are enjoined by this Court.

29. Polaris PowerLED will be irreparably harmed unless a permanent injunction is issued enjoining Nintendo and its agents, employees, representatives, affiliates, and others acting in concert with Nintendo from infringing the '117 Patent.

PRAYER FOR RELIEF

WHEREFORE, Polaris PowerLED requests the following relief from this Court:

(A) A judgment that Defendants have directly infringed one or more claims of the'117 Patent literally and/or under the doctrine of equivalents;

(B) A judgment that Defendants have indirectly infringed one or more claims of the '117 Patent literally and/or under the doctrine of equivalents by inducing infringement;

(C) Compensatory damages in an amount according to proof, and in any event no less than a reasonable royalty, including all pre-judgment and post-judgment interest at the maximum rate allowed by law;

(D)

Treble damages for willful infringement pursuant to 35 U.S.C. § 284;

(E) A judgment that this is an exceptional case and awarding Polaris PowerLED its costs and reasonable attorneys' fees incurred in this action as provided by 35 U.S.C. § 285;

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1	(F) An order and judgment permanently enjoining Nintendo and its officers,			
2	directors, agents, servants, employees, affiliates, attorneys, and all others acting in privity or			
3	in concert with them, and their parents, subsidiaries, divisions, successors and assigns from			
4	further acts of infringement of the '117 Patent; and			
5	(G) A judgment granting Polaris PowerLED such further relief as the Court may			
6	deem just and proper.			
7	JURY TRIAL DEMAND			
8	Polaris PowerLED hereby demands trial by jury on all issues so triable pursuant to			
9	Fed. R. Civ. P. 38.			
10				
11				
12	DATED: March 29, 2022. KRAMER DAY ALBERTI LIM TONKOVICH & BELLOLI LLP			
13	Dry /a/ Dragoll S. Torrhouigh			
14	Russell S. Tonkovich (<i>pro hac vice</i> to be filed)			
15	Sven Raz, WSBA No. 48093 577 Airport Boulevard, Suite 250			
16	Burlingame, CA 94010 Phone: (650) 825-4300			
17	Fax: (650) 460-8443			
18	rtonkovich@kramerday.com sraz@kramerday.com			
19				
20	ARETE LAW GROUP PLLC			
21	By: <u>/s/ Jeremy E. Roller</u>			
22	Jeremy E. Roller, WSBA No. 32021 1218 Third Avenue, Suite 2100			
23	Seattle, WA 98101 Phone: (206) 428-3250			
24	Fax: (206) 428-3251			
25	jroller@aretelaw.com			
26	Attorneys for Plaintiff Polaris PowerLED Technologies, LLC			



EXHIBIT A

US008223117B2

(12) United States Patent

Ferguson

(54) METHOD AND APPARATUS TO CONTROL DISPLAY BRIGHTNESS WITH AMBIENT LIGHT CORRECTION

- (75) Inventor: Bruce R. Ferguson, Anaheim, CA (US)
- (73) Assignee: Microsemi Corporation, Irvine, CA (US)
- (*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 841 days.

This patent is subject to a terminal disclaimer.

- (21) Appl. No.: 12/336,990
- (22) Filed: Dec. 17, 2008

(65) **Prior Publication Data**

US 2009/0091560 A1 Apr. 9, 2009

Related U.S. Application Data

- (63) Continuation of application No. 11/023,295, filed on Dec. 27, 2004, now Pat. No. 7,468,722.
- (60) Provisional application No. 60/543,094, filed on Feb. 9, 2004.
- (51) Int. Cl. *G09G 3/36* (2006.01)
- (52) U.S. Cl. 345/102; 345/207

See application file for complete search history.

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(10) Patent No.: US 8,223,117 B2

(45) **Date of Patent:** *Jul. 17, 2012

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Primary Examiner — Kevin M Nguyen

(74) Attorney, Agent, or Firm — Knobbe Martens Olson & Bear, LLP

(57) ABSTRACT

An ambient light sensor produces a current signal that varies linearly with the level of ambient light. The current signal is multiplied by a user dimming preference to generate a brightness control signal that automatically compensates for ambient light variations in visual information display systems. The multiplying function provides noticeable user dimming control at relatively high ambient light levels.

20 Claims, 10 Drawing Sheets



EP





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FIG. 1

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FIG. 3









FIG. 5



FIG. 6







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FIG. 9





5

METHOD AND APPARATUS TO CONTROL DISPLAY BRIGHTNESS WITH AMBIENT LIGHT CORRECTION

CLAIM FOR PRIORITY

This application is a continuation of U.S. patent application Ser. No. 11/023,295, filed on Dec. 27, 2004 and entitled "Method and Apparatus to Control Display Brightness with Ambient Light Correction," which claims the benefit of pri-¹⁰ ority under 35 U.S.C. §119(e) of U.S. Provisional Application No. 60/543,094, filed on Feb. 9, 2004, and entitled "Information Display with Ambient Light Correction," each of which is hereby incorporated by reference herein in their entirety.

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates to brightness control in a visual information display system, and more particularly ²⁰ relates to adjusting the brightness level to compensate for changes in ambient lighting.

2. Description of the Related Art

Backlight is needed to illuminate a screen to make a visible display in liquid crystal display (LCD) applications. The abilty to read the display is hampered under conditions of high ambient room lighting. Ambient lighting reflects off the surface of the LCD and adds a bias to the light produced by the LCD, which reduces the display contrast to give the LCD a washed-out appearance. The condition can be improved by increasing the brightness of the backlight for the LCD, thereby making the light provided by the LCD brighter in comparison to the reflected light off the LCD surface. Thus, the backlight should be adjusted to be brighter for high ambient lighting conditions and less bright for low ambient lighting conditions to maintain consistent perceived brightness.

In battery operated systems, such as notebook computers, it is advantageous to reduce power consumption and extend the run time on a battery between charges. One method of reducing power consumption, and therefore extending bat-40 tery run time, is to reduce the backlight brightness of a LCD under low ambient lighting conditions. The backlight can operate at a lower brightness level for low ambient lighting conditions because light reflections caused by the ambient light are lower and produce less of a washed-out effect. It is also advantageous to turn down the backlight under low ambient lighting conditions to extend the life of light sources in the backlight system. Typically, the light sources have a longer lifetime between failures if they run at lower brightness levels.

In some LCD applications, an ambient light sensor is used in a closed-loop configuration to adjust the backlight level in response to the ambient light level. These systems usually do not take into account user preferences. These systems are crude in implementation and do not adapt well to user preferences which may vary under various levels of eye fatigue. These systems usually do not take into account user preferences. These systems are crude in implementation and do not adapt well to user prefsoft adapt well to user prefther includes combinations of a dark level bias circuit, an overdrive clamp circuit, or an automatic shutdown circuit.

SUMMARY OF THE INVENTION

In one embodiment, the present invention is a light sensor 60 control system that provides the capability for a fully automatic and fully adaptable method of adjusting display brightness in response to varying ambient lighting conditions in combination with various user preferences. For example, the mathematical product of a light sensor output and a user 65 selectable brightness control can be used to vary backlight intensity in LCD applications. Using the product of the light 2

sensor output and the user selectable brightness control advantageously offers noticeable user dimming in bright ambient levels. Power is conserved by automatically dimming the backlight in low ambient light levels. The user control feature allows the user to select a dimming contour which works in conjunction with a visible light sensor.

In one embodiment, software algorithm can be used to multiply the light sensor output with the user selectable brightness control. In another embodiment, analog or mixed-10 signal circuits can be used to perform the multiplication. Digitizing the light sensor output or digital processing to combine the user brightness contour selection with the level of ambient lighting is advantageously not needed. The light sensor control system can be autonomous to a processor for a 15 display device (e.g., a main processor in a computer system of a LCD device).

In one embodiment, a backlight system with selective ambient light correction allows a user to switch between a manual brightness adjustment mode and an automatic brightness adjustment mode. In the manual mode, the user's selected brightness preference determines the backlight brightness, and the user dims or increases the intensity of the backlight as the room ambient light changes. In the automatic mode, the user adjusts the brightness level of the LCD to a desired level, and as the ambient light changes, the backlight automatically adjusts to make the LCD brightness appear to stay consistent at substantially the same perceived level. The automatic mode provides better comfort for the user, saves power under low ambient lighting conditions, and prevents premature aging of light sources in the backlight system.

The mathematical product of a light sensor output and a user selectable brightness control can be similarly used to vary brightness in cathode ray tube (CRT) displays, plasma displays, organic light emitting diode (OLED) displays, and other visual information display systems that do not use backlight for display illumination. In one embodiment, a brightness control circuit with ambient light correction includes a visible light sensor that outputs a sensor current signal in proportion to the level of ambient light, a dimming control input determined by a user, and a multiplier circuit that generates a brightness control signal based on a mathematical product of the sensor current signal and the dimming control input. The brightness control signal is provided to a display driver (e.g., an inverter) to adjust brightness levels of one or more light sources, such as cold cathode fluorescent lamps (CCFLs) or light emitting diodes (LEDs) in a backlight system. The brightness control circuit with ambient light correction advantageously improves ergonomics by maintaining consistent brightness as perceived by the human eye. The brightness control circuit with ambient light correction also reduces power consumption to extend battery life and reduces stress on the light sources to extend product life at low ambient light levels.

In various embodiments, the brightness control circuit further includes combinations of a dark level bias circuit, an overdrive clamp circuit, or an automatic shutdown circuit. The dark level bias circuit maintains the brightness control signal above a predetermined level when the ambient light level decreases to approximately zero. Thus, the dark level bias circuit ensures a predefined (or minimum) brightness in total ambient darkness. The overdrive clamp circuit limits the brightness control signal to be less than a predetermined level. In one embodiment, the overdrive clamp circuit facilitates compliance with input ranges for the display driver. The automatic shutdown circuit turns off the light sources when the ambient light is greater than a predefined level. For example, the automatic shutdown circuit saves power by turn-

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ing off auxiliary light sources when ambient light is sufficient to illuminate a transflective display.

The visible light sensor changes (e.g., increases or decreases) linearly with the level of ambient light and advantageously has a spectral response that approximates the spec- 5 tral response of a human eye. In one embodiment, the visible light sensor uses an array of PIN diodes on a single substrate to detect ambient light. For example, an initial current in proportion to the ambient light level is generated from taking the difference between outputs of a full spectrum PIN diode 10 and an infrared sensitive PIN diode. The initial current is amplified by a series of current mirrors to be the sensor current signal. In one embodiment, the initial current is filtered (or bandwidth limited) before amplification to adjust the response time of the visible light sensor. For example, a 15 capacitor can be used to filter the initial current and to slow down the response time of the visible light sensor such that the sensor current signal remain substantially unchanged during transient variations in the ambient light (e.g., when objects pass in front of the display).

In one embodiment, the dimming control input is a pulsewidth-modulation (PWM) logic signal that a user can vary from 0%-100% duty cycle. The PWM logic signal can be generated by a microprocessor based on user preference. In one embodiment, the dimming control input indicates user 25 preference using a direct current (DC) signal. The DC signal and a saw-tooth ramp signal can be provided to a comparator to generate an equivalent PWM logic signal. The user preference can also be provided in other forms, such as a potentiometer setting or a digital signal (e.g., a binary word).

As discussed above, the multiplier circuit generates the brightness control signal using a multiplying function to correct for ambient light variations. The brightness control signal takes into account both user preference and ambient light conditions. The brightness control signal is based on the 35 mathematical product of respective signals representing the user preference and the ambient light level.

In one embodiment, the multiplier circuit includes a pair of current steering diodes to multiply the sensor current signal with a PWM logic signal representative of the user prefer- 40 ence. The sensor current signal is provided to a network of resistors when the PWM logic signal is high and is directed away from the network of resistors when the PWM logic signal is low. The network of resistors generates and scales the brightness control signal for the backlight driver. At least 45 one capacitor is coupled to the network of resistors and configured as a low pass filter for the brightness control signal.

In one embodiment in which the user preference is indicated by a potentiometer setting, the visible light sensor output drives a potentiometer to perform the mathematical prod- 50 uct function. For example, an isolation diode is coupled between the visible light sensor output and the potentiometer. The potentiometer conducts a portion of the sensor current signal to generate the brightness control signal. A network of resistors can also be connected to the potentiometer to scale 55 the brightness control signal. An optional output capacitor can be configured as a low pass filter for the brightness control signal.

In one embodiment in which the user preference is indicated by a digital word, the multiplier circuit includes a digi- 60 tal-to-analog converter (DAC) to receive the digital word and output a corresponding analog voltage as the brightness control signal. The sensor current signal from the visible light sensor is used to generate a reference voltage for the DAC. For example, an isolation diode is coupled between the visible 65 light sensor and a network of resistors. The network of resistors conducts the sensor current signal to generate the refer4

ence voltage. An optional capacitor is coupled to the network of resistors as a low pass filter for the reference voltage. The DAC multiplies the reference voltage by the input digital word to generate the analog voltage output.

For the purposes of summarizing the invention, certain aspects, advantages and novel features of the invention have been described herein. It is to be understood that not necessarily all such advantages may be achieved in accordance with any particular embodiment of the invention. Thus, the invention may be embodied or carried out in a manner that achieves or optimizes one advantage or group of advantages as taught herein without necessarily achieving other advantages as may be taught or suggested herein.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram of one embodiment of a brightness control circuit with ambient light correction.

FIG. 2 is a block diagram of another embodiment of a ²⁰ brightness control circuit with ambient light correction.

FIG. 3 illustrates brightness control signals as a function of ambient light levels for different user settings.

FIG. 4 is a schematic diagram of one embodiment of a brightness control circuit with a multiplier circuit to combine a light sensor output with a user adjustable PWM logic signal.

FIG. 5 illustrates one embodiment of an ambient light sensor.

FIG. 6 illustrates one embodiment of an ambient light sensor with an adjustable response time.

FIG. 7 illustrates conversion of a direct current signal to a PWM logic signal.

FIG. 8 is a schematic diagram of one embodiment of a brightness control circuit with a multiplier circuit to combine a light sensor output with a user adjustable potentiometer.

FIG. 9 is a schematic diagram of one embodiment of a brightness control circuit with a multiplier circuit to combine a light sensor output with a user adjustable digital word.

FIG. 10 is a schematic diagram of one embodiment of a brightness control circuit with automatic shut down when ambient light is above a predetermined threshold.

DETAILED DESCRIPTION OF THE EMBODIMENTS

Embodiments of the present invention will be described hereinafter with reference to the drawings. FIG. 1 is a block diagram of one embodiment of a brightness control circuit with ambient light correction. A user input (DIMMING CONTROL) is multiplied by a sum of a dark level bias (DARK LEVEL BIAS) and a light sensor output (LIGHT SENSOR) to produce a brightness control signal (BRIGHT-NESS CONTROL) for a display driver 112. In one configuration, the dark level bias and the light sensor output are adjusted by respective scalar circuits (k1, k2) 100, 102 before being added by a summing circuit 104. An output of the summing circuit 104 and the user input is provided to a multiplier circuit 106. An output of the multiplier circuit 106 can be adjusted by a third scalar circuit (k3) 108 to produce the brightness control signal. An overdrive clamp circuit 110 is coupled to the brightness control signal to limit its amplitude range at the input of the display driver 112.

The display driver 112 can be an inverter for fluorescent lamps or a LED driver that controls backlight illumination of LCDs in portable electronic devices (e.g., notebook computers, cell phones, etc.), automotive displays, electronic dashboards, television, and the like. The brightness control circuit with ambient light correction provides closed-loop adjust-

ment of backlight brightness due to ambient light variations to maintain a desired LCD brightness as perceived by the human eye. The brightness control circuit advantageously reduces the backlight brightness under low ambient light conditions to improve efficiency. A visible light sensor detects the ambient light level and generates the corresponding light sensor output. The user input can come from processors in LCD devices. The brightness control circuit with ambient light correction advantageously operates independently of the processors in the LCD devices. The display driver **112** can also be used to control display brightness in CRT displays, plasma displays, OLED displays, and other visual information display systems that do not use backlight for display illumination.

FIG. 2 is a block diagram of another embodiment of a 15 brightness control circuit with ambient light correction. A light sensor output (LIGHT SENSOR) is adjusted by a scalar circuit (**k2**) **102** and then provided to a multiplier circuit **106**. A user input (DIMMING CONTROL) is also provided to the multiplier circuit **106**. The multiplier circuit **106** outputs a 20 signal that is the product of the user input and scaled light sensor output. A summing circuit **104** adds the product to a dark level bias (DARK LEVEL BIAS) that has been adjusted by scalar circuit (**k1**) **100**. An output of the summing circuit **104** is adjusted by scalar circuit (**k3**) **108** to generate a bright-25 ness control signal (BRIGHTNESS CONTROL) for a display driver **112**. An overdrive clamp **110** is coupled to the brightness control signal to limit its amplitude range at the input of the display driver **112**.

The brightness control circuits shown in both FIGS. 1 and 30 2 automatically adjust the level of the brightness control signal in response to varying ambient light. The configuration of FIG. 2 provides a predefined level of brightness in substantially total ambient darkness and independent of the user input. For example, the output of the multiplier circuit 106, in 35 both FIGS. 1 and 2, is substantially zero if the user input is about zero. The multiplier circuit 106 can be implemented using software algorithm or analog/mixed-signal circuitry. In FIG. 2, the scaled dark level bias is added to the output of the multiplier circuit 106 to provide the predefined level of 40 brightness in this case. This feature may be desired to prevent a user from using the brightness control circuit to turn off a visual information display system.

FIG. **3** illustrates brightness control signals as a function of ambient light levels for different user settings in accordance 45 with the brightness control circuit of FIG. **1**. For example, ambient light levels are indicated in units of lux (or lumens/ square meter) on a horizontal axis (or x-axis) in increasing order. Brightness control signal levels are indicated as a percentage of a predefined (or full-scale) level on a vertical axis 50 (or y-axis).

Graph 300 shows a first brightness control signal as a function of ambient light level given a first user setting (e.g., 100% duty cycle PWM dimming input). Graph 302 shows a second brightness control signal as a function of ambient light 55 level given a second user setting (e.g., 80% duty cycle PWM dimming input). Graph 304 shows a third brightness control signal as a function of ambient light level given a third user setting (e.g., 60% duty cycle PWM dimming input). Graph 306 shows a fourth brightness control signal as a function of 60 ambient light level given a fourth user setting (e.g., 40% duty cycle PWM dimming input). Graph 308 shows a fifth brightness control signal as a function of ambient light level given a fifth user setting (e.g., 20% duty cycle PWM dimming input). Finally, graph 310 shows a sixth brightness control 65 signal as a function of ambient light level given a sixth user setting (e.g., 0% duty cycle PWM dimming input).

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Graph 310 lies substantially on top of the horizontal axis in accordance with the sixth user setting corresponding to turning off the visual information display system. For the other user settings (or user adjustable dimming levels), the brightness control signal increases (or decreases) with increasing (or decreasing) ambient light levels. The rate of increase (or decrease) depends on the user setting. For example, higher user settings cause the associated brightness control signals to increase faster as a function of ambient light level. The brightness control signal near zero lux is a function of a dark bias level and also depends on the user setting. In one embodiment, the brightness control signal initially increases linearly with increasing ambient light level and reaches saturation (or 100% of full-scale) after a predetermined ambient light level. The saturation point is different for each user setting. For example, the brightness control signal begins to saturate at about 200 lux for the first user setting, at about 250 lux for the second user setting, and at about 350 lux for the third user setting. The brightness control circuit can be designed for different saturation points and dark bias levels.

FIG. 4 is a schematic diagram of one embodiment of a brightness control circuit with a multiplier circuit to combine a light sensor output with a user adjustable PWM logic signal (PWM INPUT). For example, the user adjustable PWM logic signal varies in duty cycle from 0% for minimum user-defined brightness to 100% for maximum user-defined brightness. A microprocessor can generate the user adjustable PWM logic signal based on user input which can be adjusted in response to various levels of eye fatigue for optimal viewing comfort. In one embodiment, the user adjustable PWM logic signal is provided to an input buffer circuit **410**.

The brightness control circuit includes a visible light sensor 402, a pair of current-steering diodes 404, a network of resistors (R1, R2, R3, R4) 412, 420, 416, 418, a filter capacitor (C1) 414, and an optional smoothing capacitor (C2) 422. In one embodiment, the brightness control circuit selectively operates in a manual mode or an auto mode. The manual mode excludes the visible light sensor 402, while the auto mode includes the visible light sensor 402 for automatic adjustment of display brightness as ambient light changes. An enable signal (AUTO) selects between the two modes. For example, the enable signal is provided to a buffer circuit 400. An output of the buffer circuit 400 is coupled to an input (A) of the visible light sensor 402. The output of the buffer circuit 400 is also provided to a gate terminal of a metal-oxidesemiconductor field-effect-transistor (MOSFET) switch 428. The MOSFET switch 428 is an n-type transistor with a source terminal coupled to ground and a drain terminal coupled to a first terminal of the second resistor (R2) 420.

The pair of current-steering diodes 404 includes a first diode 406 and a second diode 408 with commonly connected anodes that are coupled to an output (B) of the visible light sensor 402. The first resistor (R1) 412 is coupled between the respective cathodes of the first diode 406 and the second diode 408. An output of the input buffer circuit 410 is coupled to the cathode of the first diode 406. The filter capacitor 414 is coupled between the cathode of the second diode 408 and ground. A second terminal of the second resistor 420 is coupled to the cathode of the second diode 408. The optional smoothing capacitor 422 is coupled across the second resistor 420. The third and fourth resistors 416, 418 are connected in series between the cathode of the second diode 408 and ground. The commonly connected terminals of the third and fourth resistors 416, 418 provide a brightness control signal to an input (BRITE) of a display driver (e.g., a backlight driver) 424. In one embodiment, the display driver 424 delivers

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power to one or more light sources (e.g., fluorescent lamps) **426** coupled across its outputs.

In the auto mode, the enable signal is logic high and the buffer circuit **400** also outputs logic high (or VCC) to turn on the visible light sensor **402** and the MOSFET switch **428**. The 5 visible light sensor **402** outputs a sensor current signal in proportion to sensed ambient light level. The sensor current signal and the user adjustable PWM logic signal are multiplied using the pair of current-steering diodes **404**. For example, when the user adjustable PWM logic signal is high, 10 the sensor current signal flows through the second diode **408** towards the brightness control signal (or output). When the user adjustable PWM logic signal is low, the sensor current signal flows through the first diode **406** away from the output or into the input buffer circuit **410**. The equation for the 15 brightness control signal (BCS1) in the auto mode is:

$$\begin{split} BCS1 = dutycycle \times \left[\left(\frac{VCC \times R2 \times R4}{\left[(R1 + R2) \times (R3 + R4) \right] + (R1 \times R2)} \right) + \\ \left(\frac{ISRC \times R1 \times R2 \times R4}{\left[(R1 + R2) \times (R3 + R4) \right] + (R1 \times R2)} \right) \right]. \end{split}$$

The term "dutycycle" corresponds to the duty cycle of the user adjustable PWM logic signal. The term "VCC" corresponds to the logic high output from the input buffer circuit **410**. The term "ISRC" corresponds to the sensor current signal. The first major term within the brackets corresponds to a scaled dark bias level of the brightness control signal in total ambient darkness. The second major term within the brackets introduces the effect of the visible light sensor **402**. The network of resistors **412**, **420 416**, **418** helps to provide the dark bias level and to scale the product of the sensor current signal and the user adjustable PWM logic signal. 35

For example, the first resistor **412** serves to direct some current from the input buffer circuit **410** to the output in total ambient darkness. The second, third, and fourth resistors **420**, **416**, **418** provide attenuation to scale the brightness control signal to be compatible with the operating range of the display driver **424**. The filter capacitor **414** and the optional smoothing capacitor **422** slow down the response time of the backlight brightness control circuit to reduce flicker typically associated with indoor lighting sources. In the auto mode, the brightness control signal clamps when the voltage at the cathode of the second diode **408** approaches the compliance voltage of the visible light sensor **402** plus a small voltage drop across the second diode **408**.

In the manual mode, the enable signal is logic low. Consequently, the visible light sensor **402** and the MOSFET switch **428** are off The pair of current-steering diodes **404** isolates the visible light sensor **402** from the rest of the circuit. The off-state of the MOSFET switch **428** removes the influence of the second resistor **420** and the optional smoothing capacitor **508** filters the initial current generated by the light detector **500** and advantageously provides the ability to adjust the response time of the ambient light sensor (e.g., by changing the value of the program capacitor **508**). In a closed loop system, such as automatic brightness control signal (BCS2) in the manual mode is:

$$BCS2 = VCC \times dutycycle \times \frac{R4}{(R1 + R3 + R4)}.$$

In the manual mode, the filter capacitor **414** filters the user adjustable PWM logic signal. The brightness control circuit has an option of having two filter time constants, one for the manual mode and one for the auto mode. The time constant 65 for the manual mode is determined by the filter capacitor **414** in combination with the first, third and fourth resistors **412**, 8

416, **418**. The node impedance presented to the filter capacitor **414** is typically high during the manual mode. The time constant for the auto mode can be determined by the optional smoothing capacitor **422**, which is typically larger in value, to slow down the response of the visible light sensor **402**. The node impedance presented to the optional smoothing capacitor **422** is typically low. The optional smoothing capacitor **422** may be eliminated if the visible light sensor **402** is independently bandwidth limited.

FIG. 5 illustrates one embodiment of an ambient light sensor. The ambient light sensor includes a light detector 500, a first transistor 502, a second transistor 504 and an additional current amplifier circuit 506. The light detector 500 generates an initial current in response to sensed ambient light. The first transistor 502 and the second transistor 504 are configured as current mirrors to respectively conduct and duplicate the initial current. The second transistor 504 can also provide amplification of the duplicated initial current. The additional current amplifier circuit 506 provides further amplification of the second transistor 504 to generate a sensor current signal at an output of the ambient light sensor.

For example, the light detector (e.g., a photodiode or an array of PIN diodes) 500 is coupled between an input (or power) terminal (VDD) and a drain terminal of the first transistor 502. The first transistor 502 is an n-type MOSFET connected in a diode configuration with a source terminal coupled to ground. The first transistor 502 conducts the initial current generated by the light detector 500. The second transistor 504 is also an n-type MOSFET with a source terminal coupled to ground. Gate terminals of the first and second transistors 502, 504 are commonly connected. Thus, the second transistor 504 conducts a second current that follows the initial current and is scaled by the geometric ratios between the first and second transistors 502, 504. The additional current amplifier circuit 506 is coupled to a drain terminal of the second transistor 504 to provide amplification (e.g., by additional current mirror circuits) of the second current. The output of the additional current amplifier circuit 506 (i.e., the sensor current signal) is effectively a multiple of the initial current generated by the light detector 500.

FIG. 6 illustrates one embodiment of an ambient light sensor with an adjustable response time. The ambient light sensor of FIG. 6 is substantially similar to the ambient light sensor of FIG. 5 and further includes a program capacitor 508 and source degeneration resistors 510, 512. For example, the source degeneration resistors 510, 512 are inserted between ground and the respective source terminals of the first and second transistors 502, 504. The program capacitor 508 is coupled between the source terminal of the first transistor 502 and ground.

The program capacitor **508** filters the initial current generated by the light detector **500** and advantageously provides the ability to adjust the response time of the ambient light sensor (e.g., by changing the value of the program capacitor **508**). In a closed loop system, such as automatic brightness control for a computer display or television, it may be desirable to slow down the response time of the ambient light sensor so that the automatic brightness control is insensitive to passing objects (e.g., moving hands or a person walking by). A relatively slower response by the ambient light sensor allows the automatic brightness control to transition between levels slowly so that changes are not distracting to the viewer.

The response time of the ambient light sensor can also be slowed down by other circuitry downstream of the ambient light sensor, such as the optional smoothing capacitor **422** in the brightness control circuit of FIG. **4**. The brightness control circuit of FIG. **4** has two filter time constants, one for the

manual mode in which the visible light sensor **402** is not used and another for the auto mode which uses the visible light sensor **402**. In one embodiment, the optional smoothing capacitor **422** is included in the auto mode to slow down the response time of the brightness control circuit to accommo-5 date the visible light sensor **402**.

The optional smoothing capacitor **422** may have an unintentional side effect of slowing down the response time of the brightness control circuit to the user adjustable PWM logic signal. This unintentional side effect is eliminated by using 10 the program capacitor **508** to separately and independently slow down the response time of the ambient light sensor to a desired level. The optional smoothing capacitor **422** can be eliminated from the brightness control circuit which then has one filter time constant for both the auto and manual modes. 15

The program capacitor **508** can be coupled to different nodes in the ambient light sensor to slow down response time. However, it is advantageous to filter (or limit the bandwidth of) the initial current rather than an amplified version of the initial current because the size and value of the program 20 capacitor **508** can be smaller and lower, therefore more cost-efficient.

FIG. 7 illustrates conversion of a DC signal (DC DIM-MING INPUT) to a PWM logic signal (PWM INPUT). The DC signal (or DC dimming interface) is used in some back- 25 light systems to indicate user dimming preference. In one embodiment, a comparator **700** can be used to convert the DC signal to the PWM logic signal used in the brightness control circuit of FIG. **4**. For example, the DC signal is provided to a non-inverting input of the comparator **700**. A periodic sawtooth signal (SAWTOOTH RAMP) is provided to an inverting input of the comparator **700**. The periodic saw-tooth signal can be generated using a C**555** timer (not shown). The comparator **700** outputs a PWM signal with a duty cycle determined by the level of the DC signal. Other configurations to convert the DC signal to the PWM logic signal are also possible.

FIG. 8 is a schematic diagram of one embodiment of a brightness control circuit with a multiplier circuit to combine a light sensor output with a user adjustable potentiometer 40 (R3) 812. Some display systems use the potentiometer 812 for user dimming control. The brightness control circuit configures a visible light sensor 802 to drive the potentiometer 812 with a current signal proportional to ambient light to generate a brightness control signal (BRIGHTNESS CON- 45 TROL) at its output.

For example, the potentiometer **812** has a first terminal coupled to ground and a second terminal coupled to a supply voltage (VCC) via a first resistor (**R1**) **810**. A second resistor (**R2**) **808** in series with a p-type MOSFET switch **806** are 50 coupled in parallel with the first resistor **810**. The second terminal of the potentiometer **812** is also coupled to an output of visible light sensor **802** via an isolation diode **804**. The isolation diode **804** has an anode coupled to the output of the visible light sensor **802** and a cathode coupled to the second 55 terminal of the potentiometer **812**. A fourth resistor (**R4**) **814** is coupled between the second terminal of the potentiometer **812** and the output of the brightness control circuit. A capacitor (Cout) **816** is coupled between the output of the brightness control circuit and ground.

In one embodiment, the brightness control circuit of FIG. 8 selectively operates in an auto mode or a manual mode. An enable signal (AUTO) indicates the selection of operating mode. The enable signal is provided to a buffer circuit 800, and an output of the buffer circuit 800 is coupled to an input of the visible light sensor 802 and a gate terminal of the p-type MOSFET switch 806. When the enable signal is logic high to 10

indicate operation in the auto mode, the buffer circuit **800** turns on the visible light sensor **802** and disables (or turns off) the p-type MOSFET switch **806**. Turning off the p-type MOSFET switch **806** effectively removes the second resistor **808** from the circuit. The equation for the brightness control signal (BCS3) at the output of the brightness control circuit during auto mode operation is:

$$BCS3 = \left[VCC \times \frac{R3}{(R1 + R3)}\right] + \left[ISRC \times \frac{(R1 \times R3)}{(R1 + R3)}\right]$$

The first major term in brackets of the above equation corresponds to the brightness control signal in total ambient darkness. The second major term in brackets introduces the effect of the visible light sensor **802**. The maximum range for the brightness control signal in the auto mode is determined by the compliance voltage of the visible light sensor **802**.

The enable signal is logic low to indicate operation in the manual mode, and the buffer circuit **800** turns off the visible light sensor **802** and turns on the p-type MOSFET switch **806**. Turning on the p-type MOSFET switch **806** effectively couples the second resistor **808** in parallel with the first resistor **810**. The equation for the brightness control signal (BCS4) at the output of the brightness control circuit during manual mode operation is:

$$BCS4 = VCC \times \frac{R3 \times (R1 + R2)}{(R1 \times R2) + (R1 \times R3) + (R2 \times R3)}.$$

FIG. 9 is a schematic diagram of one embodiment of a brightness control circuit with a multiplier circuit to combine a light sensor output with a user adjustable digital word. Some display systems use a DAC 918 for dimming control. A binary input ($bn \dots b1$) is used to indicate user dimming preference. The DAC 918 generates an analog voltage (Vout) corresponding to the binary input. The analog voltage is the brightness control signal at an output of the brightness control circuit. In one embodiment, a voltage clamp circuit 920 is coupled to the output brightness control signal.

The value of the analog voltage also depends on a reference voltage (Vref) of the DAC 918. In one embodiment, the reference voltage is generated using a sensor current signal from a visible light sensor 902 that senses ambient light. For example, the visible light sensor 902 drives a network of resistors (R1, R2, R3) 906, 902, 912 through an isolation diode 904. An output of the visible light sensor 902 is coupled to an anode of the isolation diode 904. The first resistor (R1) 906 is coupled between a supply voltage (VCC) and a cathode of the isolation diode 904. The second resistor (R2) 908 is coupled in series with a semiconductor switch 910 between the cathode of the isolation diode 904 and ground. The third resistor (R3) 912 is coupled between the cathode of the isolation diode 904 and ground. An optional capacitor 914 is coupled in parallel with the third resistor 912 to provide filtering. An optional buffer circuit 916 is coupled between the cathode of the isolation diode 904 and the reference voltage input of the DAC 918.

The brightness control circuit of FIG. 9 can be configured for manual mode operation with the visible light sensor 902 disabled or for auto mode operation with the visible light sensor 902 enabled. An enable signal (AUTO) is provided to a buffer circuit 900 to make the selection between auto and manual modes. An output of the buffer circuit 900 is provided

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to an input of the visible light sensor 902 and to a gate terminal of the semiconductor switch 910.

When the enable signal is logic high to select auto mode operation, the visible light sensor 902 is active and the semiconductor switch **910** is on to effectively couple the second 5resistor 908 in parallel with the third resistor 912. In the auto mode, the equation for the brightness control signal (BCS5) at the output of the DAC 918 is:

BCS5 =

binary % fullscale×
$$\left[\left(\frac{[VCC \times (R2 \times R3)] + [ISRC \times R1 \times R2 \times R3]}{(R1 \times R2) + (R1 \times R3) + (R2 \times R3)}\right)\right]$$
.

When the enable signal is logic low to select manual mode operation, the visible light sensor 902 is disabled and the semiconductor switch 910 is off to effectively remove the second resistor 908 from the circuit. In the manual mode, the 20 equation for the brightness control signal (BCS6) at the output of the DAC 918 is:

$$BCS6 = binary \% fullscale \times VCC \times \frac{R3}{(R1 + R3)}.$$

FIG. 10 is a schematic diagram of one embodiment of a brightness control circuit with automatic shut down when ambient light is above a predetermined threshold. When light- 30 ing transflective displays, it may be preferred to shut off auxiliary light sources (e.g., backlight or frontlight) when ambient lighting is sufficient to illuminate the display. In addition to generating the brightness control signal (BRIGHTNESS CONTROL), the brightness control circuit 35 of FIG. 10 includes a shut down signal (SHUT OFF) to disable the backlight or the frontlight when the ambient light level is above the predetermined threshold.

The brightness control circuit of FIG. 10 advantageously uses a visible light sensor 1000 with two current source out- 40 puts that produce currents that are proportional to the sensed ambient light. The two current source outputs include a sourcing current (SRC) and a sinking current (SNK). The sourcing current is used to generate the brightness control signal. By way of example, the portion of the circuit generating the 45 dark level bias is provided to the multiplier such that the brightness control signal is substantially similar to the brightness control circuit shown in FIG. 4 and is not further discussed.

The sinking current is used to generate the shut down signal. In one embodiment, a comparator 1014 generates the 50 shut down signal. A resistor (R6) 1002 is coupled between a selective supply voltage and the sinking current output of the visible light sensor 1000 to generate a comparison voltage for an inverting input of the comparator 1014. A low pass filter capacitor (C3) 1004 is coupled in parallel with the resistor 55 1002 to slow down the reaction time of the sinking current output to avoid triggering on 60 hertz light fluctuations. A resistor (R7) 1006 coupled in series with a resistor (R8) 1012 between the selective supply voltage and ground generates a threshold voltage for a non-inverting input of the comparator 60 1014. A feedback resistor (R9) coupled between an output of the comparator 1014 and the non-inverting input of the comparator 1014 provides hysteresis for the comparator 1014. A pull-up resistor (R10) is coupled between the selective supply voltage and the output of the comparator 1014. The selective 65 supply voltage may be provided by the output of the buffer circuit 400 which also enables the visible light sensor 1000.

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When the ambient level is relatively low, the sinking current is relatively small and the voltage drop across the resistor 1002 conducting the sinking current is correspondingly small. The comparison voltage at the inverting input of the comparator 1014 is greater than the threshold voltage at the non-inverting input of the comparator, and the output of the comparator 1014 is low. When the ambient level is relatively high, the sinking current is relatively large and the voltage drop across the resistor 1002 is also large. The comparison 10 voltage at the inverting input of the comparator 1014 becomes less than the threshold voltage and the comparator 1014 outputs logic high to activate the shut down signal. Other configurations may be used to generate the shut down signal based on the sensed ambient light level.

While certain embodiments of the inventions have been described, these embodiments have been presented by way of example only, and are not intended to limit the scope of the inventions. Indeed, the novel methods and systems described herein may be embodied in a variety of other forms; furthermore, various omissions, substitutions and changes in the form of the methods and systems described herein may be made without departing from the spirit of the inventions. The accompanying claims and their equivalents are intended to cover such forms or modifications as would fall within the 25 scope and spirit of the inventions.

What is claimed is:

1. A brightness control circuit with selective ambient light correction comprising:

- a first input configured to receive a user signal indicative of a user selectable brightness setting;
- a light sensor configured to sense ambient light and to output a sensing signal indicative of the ambient light level;
- a multiplier configured to selectively generate a combined signal based on both the user signal and the sensing signal; and
- a dark level bias configured to adjust the combined signal to generate a brightness control signal that is used to control a brightness level of a visible display such that the brightness control signal is maintained above a predetermined level when the ambient light level decreases to approximately zero.

2. The brightness control circuit of claim 1, wherein the amount of adjustment to the combined signal is dependent on the user selectable brightness setting.

3. The brightness control circuit of claim 2, wherein the multiplier multiplies a sum of the user signal and the sensing signal by the dark level bias to generate an output signal corresponding to the brightness control signal.

4. The brightness control circuit of claim 1, wherein the dark level bias is added to the combined signal such that the amount of adjustment to the combined signal is independent of the user selectable brightness setting.

5. The brightness control circuit of claim 4, wherein the dark level bias is added to an output of the multiplier.

6. The brightness control circuit of claim 1, further comprising an overdrive clamp circuit coupled to the brightness control signal to limit its amplitude to a predefined range.

7. The brightness control circuit of claim 1, wherein the brightness control signal is provided to a display driver to control backlight illumination of a liquid crystal display.

8. The brightness control circuit of claim 7, further comprising a shut down circuit configured to turn off the display driver when the sensing signal is above a predetermined threshold.

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9. The brightness control circuit of claim 1, further comprising a second input configured to receive a selection signal to selectively operate the brightness control circuit in an auto mode or a manual mode, wherein the selection signal enables the light sensor in the auto mode and disables the light sensor 5 in the manual mode.

10. The brightness control circuit of claim 1, wherein the multiplier is implemented with a pair of current-steering diodes having commonly connected anodes coupled to the sensing signal and respective cathodes coupled to the user signal and a network of resistors to generate the brightness control signal.

11. The brightness control circuit of claim **1**, wherein the user signal corresponds to a setting of a user adjustable potentiometer, and the multiplier is implemented with an isolation diode having an anode coupled to the sensing signal and a cathode coupled to the user adjustable potentiometer and a network of resistors to generate the brightness control signal.

12. The brightness control circuit of claim **1**, wherein the ²⁰ user signal corresponds to a digital word, and the multiplier is implemented with a digital-to-analog converter configured to receive the digital word and a reference signal determined by the sensing signal to generate the brightness control signal.

13. The brightness control circuit of claim **1**, wherein the 25 light sensor comprises a full spectrum PIN diode, an infrared sensitive PIN diode, and an amplifier configured to generate the sensing signal based on a difference between an output of the full spectrum PIN diode and an output of the infrared sensitive PIN diode. 30

14. The brightness control circuit of claim 13, wherein the light sensor further comprises a low pass filter to reduce sensitivity to transient variations of ambient light.

15. A method to selectively provide ambient light correction, said method comprising:

receiving a user input signal indicative of a user selectable brightness setting;

- selectively multiplying the input signal with a sense signal to generate a combined signal, wherein the sense signal indicates an ambient light level; and
- adjusting the combined signal with a dark level bias to generate a brightness control signal for controlling brightness of a visible display such that the brightness control signal is maintained above a predetermined level when the ambient light level decreases to approximately zero.

16. The method of claim 15, wherein the step of selectively multiplying the input signal with the sense signal is performed by a software algorithm, an analog circuit, or a mixed-signal circuit.

17. The method of claim 15, wherein the dark level bias is added to the sense signal before selective multiplication such that the amount of adjustment to the combined signal is dependent on the input signal.

18. The method of claim **15**, wherein the dark level bias is added to the combined signal after selective multiplication such that the amount of adjustment to the combined signal is independent of the input signal and the sense signal.

19. A brightness control circuit comprising:

- means for receiving an input signal indicative of a user selectable brightness setting;
- means for sensing ambient light to generate a sense signal indicative of the ambient light level;
- means for multiplying the input signal with the sense signal to generate a combined signal; and
- means for adjusting the combined signal with a dark level bias to generate a brightness control signal that is maintained above a minimum level when the ambient light level decreases to approximately zero.

20. The brightness control circuit of claim **19**, further comprising means for selectively operating in a manual mode or an auto mode, wherein the means for sensing ambient light is enabled in the auto mode and disabled in the manual mode.

* * * * *

EXHIBIT B

Technical Specs - Nintendo Switch[™] - System hardware, console specs -... https://www.nint Case 2:22-cv-00386-JLR Document 1 Filed 03/29/22 Page 39 of 58

V

Tech specs

Technical Specs



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Nintendo Switch[™] – OLED Model



Size	4 inches high, 9.5 inches long, and 0.55 inches deep (with Joy-Con attached) *The depth from the tip of the analog sticks to the tip of the ZL/ZR buttons is 1.12 inches
Weight	Approximately .71 lbs (Approximately .93 lbs with Joy-Con controllers attached)
Screen	Multi-touch capacitive touch screen / 7.0 inch OLED screen / 1280x720
CPU/GPU	NVIDIA Custom Tegra processor
Storage	64 GB Users can easily expand storage space using microSDHC or microSDXC cards up to 2TB (sold separately).

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Wireless	Wi-Fi (IEEE 802.11 a/b/g/n/ac compliant) / Bluetooth 4.1
Video output	Up to 1080p via HDMI in TV mode Up to 720p via built-in screen in Tabletop mode and Handheld modes
Audio output	Compatible with 5.1ch Linear PCM output Output via HDMI connector in TV mode
Speakers	Stereo
Buttons	Power button / Volume button
USB connector	USB Type-C Used for charging or for connecting to the Nintendo Switch dock.
Headphone/mic jack	3.5mm 4-pole stereo (CTIA standard)
Game card slot	Nintendo Switch game cards
microSD card slot	Compatible with microSD, microSDHC, and microSDXC memory cards *Once the microSDXC card is inserted, a system update will be necessary. An internet connection is required to perform this system update.

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Sensor	Accelerometer, gyroscope, and brightness sensor
Operating environment	41-95 degrees F / 20-80% humidity
Internal battery	Lithium-ion battery / 4310mAh
Battery life	Approximately 4.5 - 9 hours The battery life will depend on the games you play. For instance, the battery will last approximately 5.5 hours for <i>The Legend of Zelda: Breath of the Wild</i> .
Power Consumption	TV mode Playing a game: Approx. 6W Watching a video: Approx. 5W On the main menu: Approx. 3W Sleep mode*: Approx. 0.3W Sleep mode**: Approx. 2.2W Power off: Approx. 0.3W Handheld & Tabletop modes Playing a game: Approx. 4W Watching a video: Approx. 3W On the main menu: Approx. 2W Sleep mode: Approx. 0.03W Power off: Less than 0.01W *Wired internet connection setting is off **Wired internet connection setting is on Power consumption varies depending on software and usage conditions
Charging time	Approximately 3 hours *When charging while the hardware is in sleep mode

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Nintendo SwitchTM console (HAC-001(-01))



Size	Approximately 4 inches high, 9.4 inches long, and 0.55 inches deep (with Joy-Con attached) *The depth from the tip of the analog sticks to the tip of the ZL/ZR buttons is 1.12 inches
Weight	Approximately .66 lbs (Approximately .88 lbs when Joy-Con controllers are attached)
Screen	Multi-touch capacitive touch screen / 6.2-inch LCD Screen / 1280 x 720
CPU/GPU	NVIDIA Custom Tegra processor
Storage	32 GB of internal storage, a portion of which is reserved for use by the system. Users can easily expand storage space using microSDHC or microSDXC cards up to 2TB (sold separately).
Wireless	Wi-Fi (IEEE 802.11 a/b/g/n/ac)(*) Bluetooth 4.1 (*In TV mode, Nintendo Switch systems can be connected with a wired LAN adapter - sold separately)

Video output	Up to 1080p via HDMI in TV mode Up to 720p via built-in screen in tabletop mode and handheld mode
Audio output	Compatible with 5.1ch Linear PCM output Output via HDMI connector in TV mode
Speakers	Stereo
Buttons	Power button / Volume button
USB connector	USB Type-C Used for charging or for connecting to the Nintendo Switch dock.
Headphone/mic jack	3.5mm 4-pole stereo (CTIA standard)
Game card slot	Nintendo Switch game cards
microSD card slot	Compatible with microSD, microSDHC, and microSDXC memory cards *Once the microSDXC card is inserted, a system update will be necessary. An Internet connection is required to perform this system update.
Sensor	Accelerometer, gyroscope, and brightness sensor

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Operating environment	41-95 degrees F / 20-80% humidity
Internal battery	Lithium-ion battery/4310mAh
Battery life	Approximately 4.5 - 9 hours *The battery life will depend on the games you play. For instance, the battery will last approximately 5.5 hours for <i>The Legend of Zelda:</i> <i>Breath of the Wild</i> .
Power Consumption	TV mode Playing a game: Approx. 7W Watching a video: Approx. 6W On the main menu: Approx. 3W Sleep mode: Approx. 0.3W Power off: Approx. 0.3W Handheld & Tabletop modes Playing a game: Approx. 4W Watching a video: Approx. 4W Watching a video: Approx. 3W On the main menu: Approx. 2W Sleep mode: Approx. 0.03W Power off: Less than 0.01W Power consumption varies depending on software and usage conditions
Charging time	Approximately 3 hours *When charging while the hardware is in sleep mode

Nintendo Switch Lite (HDH-001)



Size	Approximately 3.6 inches high, 8.2 inches long, and .55 inches deep *The depth from the tip of the analog sticks to the tip of the ZL/ZR buttons is 1.12 inches
Weight	Approx61 lbs
Screen	Capacitive touch screen / 5.5 inch LCD / 1280x720 resolution
CPU/GPU	NVIDIA Custom Tegra processor
Storage	32 GB of internal storage, a portion of which is reserved for use by the system. Users can easily expand storage space using microSDHC or microSDXC cards up to 2TB (sold separately).
Wireless	Wi-Fi (IEEE 802.11 a/b/g/n/ac Bluetooth 4.1 / NFC (near field communication)
Speakers	Stereo
Buttons	Left Stick Right Stick A B X Y L R ZL ZR + - Buttons + Control Pad Power Button Volume Button HOME Button Capture Button

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USB connector	USB Type-C Used for charging only.
Headphone/mic jack	3.5mm 4-pole stereo (CTIA standard)
Game card slot	Nintendo Switch game cards
microSD card slot	Compatible with microSD, microSDHC and microSDXC memory cards. *Once the microSDXC card is inserted, a system update will be necessary. An Internet connection is required to perform this system update.
Sensor	Accelerometer / gyroscope
Operating environment	41-95 degrees F / 20-80% humidity
Internal battery	Lithium ion battery / battery capacity 3570mAh
Battery life	Model number: HDH-001 Approx. 3.0 to 7.0 hours *The battery life will depend on the games you play. For instance, the battery will last approximately 4 hours for <i>The Legend of Zelda:</i> <i>Breath of the Wild</i> .
Power Consumption	Handheld & Tabletop modes <i>Playing a game:</i> Approx. 4W <i>Watching a video:</i> Approx. 3W <i>On the main menu:</i> Approx. 2W

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	<i>Sleep mode:</i> Approx. 0.03W <i>Power off:</i> Less than 0.01W
	Power consumption varies depending on software and usage conditions
Charging time	Approximately 3 hours *When charging while the hardware is in sleep mode

Nintendo Switch dock with LAN port



Size	Approximately 4.1 inches high, 6.9 inches long, and 2.0 inches deep
Weight	Approximately .69 lbs
Output	USB Port (USB 2.0 compatible) x2 on the side System connector AC adapter port HDMI port Wired LAN port (LAN cable sold separately.)

Nintendo Switch[™] dock



Size	Approximately 4.1 inches high, 6.8 inches long, and 2.12 inches deep
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Weight	Approximately .72 lbs
Output	USB Port (USB 2.0 compatible) x2 on the side, 1 on the back System connector AC adapter port HDMI port

Joy-Con[™] controllers



Joy-Con [L]

Joy-Con [R]

Size	Approximately 4.1 inches high, 6.8 inches long, and 2.12 inches deep	
Weight	1.7 oz	1.8 oz
Buttons	Left Stick Up down left right L ZL SL SR - Buttons Capture Button Release Button Sync Button	Right Stick A B X Y R ZR SL SR + Buttons HOME Button Release Button Sync Button
Wireless	Bluetooth 3.0	Bluetooth 3.0/NFC
Sensor	Accelorometer Gyroscope	Accelorometer Gyroscope IR Motion Camera

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Vibration	HD Rumble
Battery Life	Approximately 20 hours *This is an estimate. The battery life may be shortened depending on the usage.
Charging Time	Approximately 3 hours 30 minutes * Joy-Con controllers are charged by attaching them to a console or charging grip connected to a power supply.

Joy-Con Strap

Size	Approximately 3.98 inches high, 0.57 inches long, and 0.55 inches deep
Weight	Approximately .66 oz

Joy-Con Grip



Size	Approximately 3.98 inches high, 5.67 inches long, and 1.58 inches deep
Weight	Approximately .21 lbs

https://www.nintendo.com/switch/tech-specs/





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EXHIBIT C

Nintendo Support: How to Adjust Screen Brightness

https://en-americas-support.nintendo.com/app/answers/detail/a_id/22330...

💄 Log in

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Customer Support



From Within System Settings.

https://en-americas-support.nintendo.com/app/answers/detail/a_id/22330...

Case 2:22-cv-063& The BME Meau, Meter System Settings, 2862 Screen Bight Fresh, then Auto-

Brightness Adjustment, and finally select **On** or manually adjust the screen brightness by sliding the brightness level bar.

Support/Health & Safety	Auto-Brightness Adjustment	ON
Airplane Mode	*	
Screen Brightness	\blacklozenge This can also be changed from Quick Settings (hold ®).	
Screen Lock		
Parental Controls		
Internet		
Data Management		
	Bac	

Was this helpful?

No

Yes

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Change Language



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EXHIBIT D

[Switch] How can I adjust the screen brightness? | Q&A | Support | Nintendo https://www.nintendo.com/sg/support/qa/detail/34040 Case 2:22-cv-00386-JLR Document 1 Filed 03/29/22 Page 57 of 58

Support/Health & Safety	Auto-Brightness	On
Airplane Mode	*	
Screen Brightness	This can also be changed from Quick Settings (here)	old ®).
Screen Lock		
Parental Controls		
Internet		
Data Management		

* You can also alter the brightness via Quick Settings (accessed by holding the HOME Button). * When playing in TV Mode, the "Screen Brightness" setting cannot be changed.

See all Q&A list