	Case 3:22-cv-03781-AGT Documer	nt 1 Filed 06/27/22 Page 1 of 9						
1 2 3 4 5 6 7	Dmitry Kheyfits (SBN 321326) dkheyfits@kblit.com KHEYFITS BELENKY LLP 4 Embarcadero Center, Suite 1400 San Francisco, CA 94111 Tel: 415-429-1739 Fax: 415-429-6347 <i>Attorneys for USB Bridge Solutions, LLC</i> UNITED STATES DI	STRICT COURT						
8 9	NORTHERN DISTRICT OF CALIFORNIA							
10	USB BRIDGE SOLUTIONS, LLC,	Case No.: 22-cv-3781						
11	Plaintiff,							
12 13	V.	COMPLAINT FOR PATENT						
14	SILICON POWER COMPUTER &	INFRINGEMENT						
15	COMMUNICATIONS INC., and							
16	SILICON POWER COMPUTER & COMMUNICATIONS USA INC.							
17 18	Defendants.	DEMAND FOR JURY TRIAL						
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COMPLAINT FOR PATENT INFRINGEMENT

1. Plaintiff USB Bridge Solutions, LLC ("USBB") hereby files this Complaint for patent infringement against Defendant Silicon Power Computer & Communications Inc. ("Silicon Power Taiwan") and Defendant Silicon Power Computer & Communications USA Inc. ("Silicon Power USA"), (jointly, "Silicon Power" or "Defendants") and alleges as follows:

PARTIES

2. Plaintiff USBB is a limited liability company organized and existing under the laws of the State of Georgia, having its principal place of business at Day Building, 4725 Peachtree Corners Circle, Suite 230, Peachtree Corners, GA 30092.

3. On information and belief, Defendant Silicon Power Taiwan is a corporation organized and existing under the laws of Taiwan having its principal place of business at 7F., No.106, Zhouzi St., Neihu District, Taipei City 114, Taiwan.

4. On information and belief, Defendant Silicon Power USA is a corporation organized and existing under the laws of California having its principal place of business at 4590 Enterprise Street, Fremont, CA 94538.

JURISDICTION AND VENUE

5. This is an action under the patent laws of the United States, 35 U.S.C.§§ 1, et seq., for infringement by Silicon Power of claims of U.S. Patent No.7,231,485.

This Court has subject matter jurisdiction pursuant to 28 U.S.C. §§
 1331 and 1338(a).

7. Silicon Power Taiwan is subject to personal jurisdiction of this Court because, *inter alia*, Silicon Power Taiwan has committed and continues to commit acts of patent infringement in the State of California, including by making, using, offering to sell, selling, and/or importing the accused products into California; (ii)

Silicon Power Taiwan purposefully supplies and directs the accused products for storage, warehousing, and sales by distributors and resellers in the State of California; and (iii) Silicon Power Taiwan delivers its products into the stream of commerce with the expectation that they will be purchased by consumers in the State of California. In addition, or in the alternative, this Court has personal jurisdiction over ADATA pursuant to Fed. R. Civ. P. 4(k)(2).

8. Silicon Power USA is subject to personal jurisdiction of this Court because, *inter alia*, on information and belief, independently and/or via its agents, (i) Silicon Power USA is a California corporation, (ii) Silicon Power USA has its principal place of business is in this Judicial District at 4590 Enterprise Street, Fremont, CA 94538; (ii) Silicon Power USA regularly conducts business in the State of California; and (iii) Silicon Power has committed and continues to commit acts of patent infringement in the State of California, including by making, using, offering to sell, selling, and/or importing the accused products into California.

9. Venue is proper as to Silicon Power Taiwan in this Judicial District under 28 U.S.C. § 1391(c) because, *inter alia*, Silicon Power Taiwan is a foreign corporation.

10. Venue is proper as to Silicon Power USA in this Judicial District under 28 U.S.C. § 1400(b) because, *inter alia*, on information and belief, Silicon Power USA maintains a regular and established place of business in this Judicial District at 4590 Enterprise Street, Fremont, CA 94538, and has committed acts of patent infringement in this Judicial District and/or has contributed to or induced acts of patent infringement by others in this District.

BACKGROUND

11. On June 12, 2007, the United States Patent and Trademark Office duly and lawfully issued U.S. Patent No. 7,231,485 ("the Patent-in-Suit" or the "'485 Patent"), entitled "Universal Serial Bus (USB) Interface For Mass Storage

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Device."

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NOTICE

12. By letter dated June 2, 2020, USBB, via its licensing agent, notified Silicon Power of the existence of the '485 Patent and attached the Memorandum Opinion and Order Regarding Claim Construction issued in *USB Bridge Solutions, LLC v. Buffalo Inc.* et al. (C.A. No. 1-17-cv-1158-LY) (W.D. Tex.).

13. By letter dated June 21, 2021, USBB followed up on the June 2, 2020 letter, identifying Silicon Power's products and further identifying a claim of the '485 Patent that these products infringed.

COUNT I: INFRINGEMENT OF THE '485 PATENT

14. Plaintiff incorporates the preceding paragraphs as if fully set forth herein.

15. On information and belief, Silicon Power has infringed, and continues to infringe, the '485 Patent pursuant to 35 U.S.C. § 271(a), literally or under the doctrine of equivalents, by making, using, offering to sell, selling in the United States or importing into the United States the portable hard drives such as the Armor A30, Armor A60, Bolt B75 Pro, and all other external and/or portable drives and drive enclosures comprising a USB interface and SATA functionality recited in the asserted claims ("Accused Products").

16. For example, on information and belief, Silicon Power has infringed at least claim 8 of the '485 Patent by making, using, offering to sell, selling in the United States or importing into the United States the Accused Products comprising a secondary board configured to enable communication between a mass storage device motherboard and a host motherboard, such as the Silicon Power Armor A30 Portable HD. *See* Ex. 1, Silicon Power Armor A60 Portable HD Datasheet; Ex. 2, Photograph showing the secondary board in the Silicon Power Armor A60 Portable HD being connected to a motherboard of a mass storage device. *See also* Ex. 7

showing a secondary board being connected to the motherboard of the mass
storage device in the B75 Pro model. *See also* Ex. 8 (showing same for the A30 model, excerpted from a teardown video available at

https://www.youtube.com/watch?v=r1h63oyeqNY). The Accused Products include a bridging System-on-Chip, such as the ASMedia ASM1153. *See* Ex. 3, Photograph of a secondary board having a bridging System-on-Chip in the Silicon Power Armor A30 Portable HD; Ex. 4, ASM1153 Webpage Description, p. 1 ("ASM1153 is the ASMedia third generation single chip solution, bridging the USB3.2 Gen1 to Serial SATA host interface with highly integrated SuperSpeed USB3.0, High Speed USDB2.0 and SATA1.5/3.0 Gbps ASMedia self -designed PHYs.").

17. The Accused Products comprise a connector port for receiving signals from the mass storage device motherboard. *See* Ex. 2, Photograph showing the secondary board in the Silicon Power Armor A60 Portable HD having a connector port.

18. The Accused Products comprise a bridging circuit for converting the signals from the mass storage device motherboard into USB signals. *See* Ex. 2, Photograph showing the secondary board in the Silicon Power Armor A60 Portable HD having a bridging System-on-Chip; Ex. 3, Photograph of the ASMedia ASM1153 bridging System-on-Chip in the Silicon Power Armor A60 Portable HD; Ex. 4, ASM1153 Webpage Description, p. 1 ("ASM1153 is the ASMedia third generation single chip solution, bridging the USB3.2 Gen1 to Serial SATA host interface with highly integrated SuperSpeed USB3.0, High Speed USDB2.0 and SATA1.5/3.0 Gbps ASMedia self -designed PHYs.").

19. The bridging circuit includes a USB physical interface transceiver.*See* Ex. 4 ASM1153 Webpage Description, p. 1 ("Compliant with USB3.0Specification Revision 1.0 . . . Compliant with USB Specification Revision 2.0");

Ex. 5 Universal Serial Bus 3.0, Specification, Revision 1.0, p. 5-33 (Showing Tx and Rx lines of a USB 3.0 connector).

20. The bridging circuit includes a serial interface engine coupled to the USB physical interface transceiver. *See* Ex. 4, ASM1153 Webpage Description, p. 1 ("Compliant with USB3.0 Specification Revision 1.0 . . . Compliant with USB Specification Revision 2.0"). The serial interface engine is coupled to the USB physical interface transceiver, such as by a bus.

21. On information and belief, the bridging circuit includes an input/output interface coupled to the serial interface engine. *See* Ex. 6, ASM1053 Datasheet (showing, for example, input/output interface being coupled to the serial interface engine). On information and belief, the relevant features and components of the ASM1053 SoC are also present in the ASM1153 SoC. The exemplary interfaces are coupled to the serial interface engine, such as by a bus.

22. On information and belief, the bridging circuit includes a RAM control circuit coupled to the input/output interface, such as RAM and buffer control circuits internal to the ASM1153. *See* Ex. 4, ASM1153 Webpage Description, p. 1 ("Integrated 8-bit micro-processor with embedded program RAM and ROM"); Ex. 6, ASM1053 Datasheet (showing RAM and the input/output interface discussed above). The exemplary RAM control circuits, included in the RAM blocks, are coupled to the input/output interface, such as by a bus.

23. On information and belief, the bridging circuit includes a global control circuit coupled to the input/output interface. For example, the Accused Products include USB Device Control, SATA Host Control, and/or microprocessor global control circuits. *See* Ex. 4, ASM1153 Webpage Description, p. 1 (" . . . ASM1153 . . . also integrates an 8-bit micro-processor and embedded RAM. . ."). The exemplary global control circuits are coupled to the input/output interface, such as by a bus. *See* Ex. 6, ASM1053 Datasheet.

24. The bridging circuit includes a translate circuit coupled to the global control circuit. For example, the microprocessor includes the translate circuit. *See* Ex. 4, ASM1153 Webpage Description, p. 1 ("Customers can easily enhance their storage device performance with ASM1153 since it also integrates an 8-bit microprocessor and embedded RAM to provide a cutting edge solution in USB to SATA device enclosure market."). The microprocessor is coupled to the global control circuit, such as by a bus.

25. The bridging circuit includes a disk interface coupled to the ram control circuit and the translate circuit. *See* Ex. 4, ASM1153 Webpage Description, p. 1 ("ASM1153 is the ASMedia third generation single chip solution, bridging the USB3.2 Gen1 to Serial SATA host interface with highly integrated SuperSpeed USB3.0, High Speed USDB2.0 and SATA1.5/3.0 Gbps ASMedia self -designed PHYs."); *see also* Ex. 6, ASM1053 Datasheet. The exemplary disk interface is coupled to the RAM control circuit and the translate circuit, such as by a bus.

26. The Accused Products include a USB connector port for outputting the USB signals to the host motherboard. *See* Ex. 2, Photograph showing secondary board in the Silicon Power Armor A60 Portable HD having a USB connector output port.

27. On information and belief, Silicon Power has induced infringement of the '485 Patent pursuant to 35 U.S.C. § 271(b), by actively and knowingly inducing, directing, causing, and encouraging others, including, but not limited to, its partners, resellers, distributers, customers, and end users, to make, use, sell, and/or offer to sell in the United States, and/or import into the United States, the Accused Products by, among other things, providing the accused products and incorporated USB Bridging technology, specifications, instructions, manuals, advertisements, marketing materials, and technical assistance relating to the

installation, set up, use, operation, and maintenance of said products.

28. On information and belief, Silicon Power has committed and continues to commit the foregoing infringing activities without a license.

29. On information and belief, Silicon Power knew the '485 Patent existed and knew of an exemplary infringing Silicon Power product while committing the foregoing infringing acts thereby willfully, wantonly and deliberately infringing the '485 Patent.

PRAYER FOR RELIEF

WHEREFORE, Plaintiff USBB prays for the judgment in its favor against Silicon Power, and specifically, for the following relief:

A. Entry of judgment in favor of USBB against Silicon Power on all counts;

B. Entry of judgment that Silicon Power has infringed the Patent-in-Suit;

C. Entry of judgment that Silicon Power's infringement of the Patent-in-Suit has been willful;

D. Award of compensatory damages adequate to compensate USBB for Silicon Power's infringement of the Patent-in-Suit, in no event less than a reasonable royalty trebled as provided by 35 U.S.C. § 284;

E. USBB's costs;

F. Pre-judgement and post-judgement interest on USBB's award; and

G. All such other and further relief as the Court deems just or equitable.

DEMAND FOR JURY TRIAL

Pursuant to Rule 38 of the Fed. R. Civ. P., Plaintiff USBB hereby demands trial by jury in this action of all claims so triable.

Respectfully Submitted,

June	27,	2022
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