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24 SEOUL SEMICONDUCTOR CO., LTD.,
25 SEOUL VIOSYS CO., LTD.

26 UNITED STATES DISTRICT COURT
27 NORTHERN DISTRICT OF CALIFORNIA

28 SEOUL SEMICONDUCTOR CO., LTD., a
Korean corporation, SEOUL VIOSYS CO.,
LTD., a Korean corporation,

Plaintiffs,

v.

FINELITE, INC.,

Defendant.

Case No. 3:22-cv-2869

**COMPLAINT FOR PATENT
INFRINGEMENT**

DEMAND FOR JURY TRIAL

1 Plaintiffs Seoul Semiconductor Co., Ltd. (“Seoul Semiconductor”) and Seoul Viosys Co.,
2 Ltd. (“Seoul Viosys”), (collectively the “Seoul Plaintiffs”) for their Complaint against Defendant
3 Finelite, Inc. (“Finelite”) allege as follows:

4 **INTRODUCTION**

5 1. The Seoul Plaintiffs bring this patent infringement action to protect their valuable
6 patented technology relating to light emitting diodes (“LEDs”) and LED lighting. An LED is a
7 semiconductor device that converts electrical energy into light. LEDs have many advantages over
8 conventional light sources, including lower energy consumption, longer lifetime, and smaller size.

9 2. Seoul Semiconductor was founded in 1992 with approximately 30 employees in a
10 small space of a commercial building in Bongchen-dong, Seoul, Korea. From those initial 30
11 employees, Seoul Semiconductor has grown into one of the largest manufacturers of LEDs in the
12 world. It’s subsidiary, Seoul Viosys, is also a leading company in the LED industry.

13 3. The Seoul Plaintiffs’ success is in large part due to their significant investment in
14 innovation and respect for intellectual property. Seoul Semiconductor has invested in research and
15 development (“R&D”) for decades. Seoul Semiconductor invests over 10% of sales revenue into
16 R&D and owns one of the largest LED patent portfolios in the world, which includes more than
17 10,000 patents worldwide.

18 **THE PARTIES**

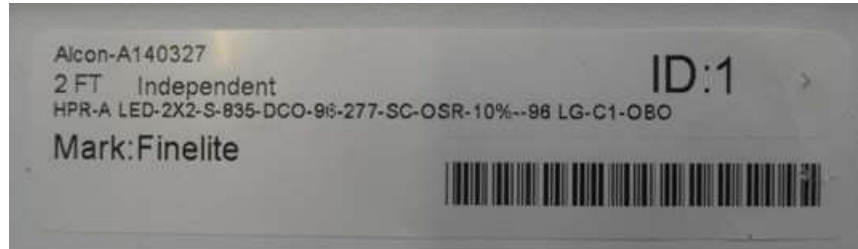
19 4. Plaintiff Seoul Semiconductor is a company organized and existing under the laws
20 of the Republic of Korea, with its principal place of business at 1B-25, 727, Wonsi-dong, Danwon-
21 gu, Ansan-city, Gyeonggi-do, Korea 425-851.

22 5. Plaintiff Seoul Viosys is a company organized and existing under the laws of the
23 Republic of Korea, with its principal place of business at 65-16, Sandan-ro 163 beon-gil, Danwon-
24 gu, Ansan-city, Gyeonggi-do, Korea 425-851. Seoul Viosys is a subsidiary of Seoul
25 Semiconductor.

26 6. Defendant Finelite, Inc. is a California corporation with its headquarters at 30500
27 Whipple Road Union City, California 94587-1530.

28 7. As alleged in below, upon information and belief, Finelite markets and sells the

1 Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO products to consumers located
2 in the California and other areas of the United States. An image of the Finelite HPR-A-2X2-S-835-
3 DCO-LED-3500K-277V-SC-C1-OBO LED panel is provided below.



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14 **JURISDICTION AND VENUE**

15 8. This Court has jurisdiction over the subject matter of this action under 28 U.S.C.
16 §§ 1331 and 1338(a) because, at the very least, this action arises under the patent laws of the United
17 States, including 35 U.S.C. § 271 et seq.

18 9. This Court has personal jurisdiction over Finelite because, upon information and
19 belief, it maintains a principal place of business in California.

20 10. Venue is proper within this judicial district under 28 U.S.C. § 1400(b) because, on
21 information and belief, Finelite resides in this district. Venue is also proper because, on information
22 and belief, Finelite has committed acts of infringement and have a regular and established place of
23 business in this district.

24 **PATENTS-IN-SUIT**

25 11. Seoul Semiconductor is the lawful owner of all right, title, and interest in United
26 States Patent No. 7,397,069 entitled “Semiconductor Device” (“the ’069 patent”), including the
27 right to sue and to recover for infringement thereof. The ’069 patent was duly and legally issued on
28 July 8, 2008, by the United States Patent and Trademark Office to Tachibana et al. A copy of the

1 '069 patent is attached hereto as Exhibit 1.

2 12. Seoul Semiconductor is the lawful owner of all right, title, and interest in United
3 States Patent No. 9,269,868 entitled “Semiconductor Light Emitting Element and Method For
4 Manufacturing Semiconductor Light Emitting Element” (“the ’868 patent”), including the right to
5 sue and to recover for infringement thereof. The ’868 patent was duly and legally issued on
6 February 23, 2016, by the United States Patent and Trademark Office to Kushibe et al. A copy of
7 the ’868 patent is attached hereto as Exhibit 2.

8 13. Seoul Viosys is the lawful owner of all right, title, and interest in United States
9 Patent No. 9,799,800 entitled “Light Emitting Device and Method of Fabricating the Same” (“the
10 ’800 patent”), including the right to sue and to recover for infringement thereof. The ’800 patent
11 was duly and legally issued on October 24, 2017, by the United States Patent and Trademark Office
12 to Jang et al. A copy of the ’800 patent is attached hereto as Exhibit 3.

13 14. Seoul Semiconductor is the lawful owner of all right, title, and interest in United
14 States Patent No. 7,667,225 entitled “Light Emitting Device” (“the ’225 patent”), including the
15 right to sue and to recover for infringement thereof. The ’225 patent was duly and legally issued on
16 February 23, 2010, by the United States Patent and Trademark Office to Lee et al. A copy of the
17 ’225 patent is attached hereto as Exhibit 4.

18 15. Seoul Viosys is the lawful owner of all right, title, and interest in United States
19 Patent No. 8,664,638 entitled “Light-Emitting Diode Having an Interlayer with High Voltage
20 Density and Method for Manufacturing the Same” (“the ’638 patent”), including the right to sue
21 and to recover for infringement thereof. The ’638 patent was duly and legally issued on March 4,
22 2014, by the United States Patent and Trademark Office to Yoo et al. A copy of the ’638 patent is
23 attached hereto as Exhibit 5.

24 16. Seoul Viosys is the lawful owner of all right, title, and interest in United States
25 Patent No. 9,716,210 entitled “Light Emitting Diode and Method of Fabricating the Same” (“the
26 ’210 patent”), including the right to sue and to recover for infringement thereof. The ’210 patent
27 was duly and legally issued on July 25, 2017, by the United States Patent and Trademark Office to
28 Kim et al. A copy of the ’210 patent is attached hereto as Exhibit 6.

1 17. Seoul Viosys is the lawful owner of all right, title, and interest in United States
2 Patent No. 10,418,514 entitled “Light Emitting Diode and Method of Fabricating the Same” (“the
3 ’514 patent”), including the right to sue and to recover for infringement thereof. The ’514 patent
4 was duly and legally issued on September 17, 2019, by the United States Patent and Trademark
5 Office to Kim et al. A copy of the ’514 patent is attached hereto as Exhibit 7.

6 18. Seoul Semiconductor is the lawful owner of all right, title, and interest in United
7 States Patent No. 8,604,496 entitled “Optical Semiconductor Device” (“the ’496 patent”), including
8 the right to sue and to recover for infringement thereof. The ’496 patent was duly and legally issued
9 on December 10, 2013, by the United States Patent and Trademark Office to Shioda et al. A copy
10 of the ’496 patent is attached hereto as Exhibit 8.

11 19. Seoul Viosys is the lawful owner of all right, title, and interest in United States
12 Patent No. 7,982,207 entitled “Light Emitting Diode” (“the ’207 patent”), including the right to sue
13 and to recover for infringement thereof. The ’207 patent was duly and legally issued on July 19,
14 2011, by the United States Patent and Trademark Office to Kim et al. A copy of the ’496 patent is
15 attached hereto as Exhibit 9.

16 20. Seoul Viosys is the lawful owner of all right, title, and interest in United States
17 Patent No. 10,672,952 entitled “Light Emitting Diode for Surface Mount Technology, Method of
18 Manufacturing the Same, And Method of Manufacturing Light Emitting Diode Module” (“the ’952
19 Patent”), including the right to sue and to recover for infringement thereof. The ’952 patent was
20 duly and legally issued on June 2, 2020, by the United States Patent and Trademark Office to Chae
21 et al. A copy of the ’952 patent is attached hereto as Exhibit 10.

22 21. Seoul Semiconductor is the lawful owner of all right, title, and interest in United
23 States Patent No. 8,981,410 entitled “Distributed Bragg Reflector for Reflecting Light of Multiple
24 Wavelengths from an LED” (“the ’410 patent”), including the right to sue and to recover for
25 infringement thereof. The ’410 patent was duly and legally issued on March 17, 2015, by the United
26 States Patent and Trademark Office to Lin. A copy of the ’410 patent is attached hereto as Exhibit
27 11.

28 22. Seoul Viosys is the lawful owner of all right, title, and interest in United States

1 Patent No. 9,577,157 entitled “Light Emitting Diode Chip Having Distributed Bragg Reflector and
2 Method of Fabricating the Same” (“the ’157 patent”), including the right to sue and to recover for
3 infringement thereof. The ’157 patent was duly and legally issued on February 21, 2017, by the
4 United States Patent and Trademark Office to Lee at al. A copy of the ’157 patent is attached hereto
5 as Exhibit 12.

6 23. Seoul Semiconductor is the lawful owner of all right, title, and interest in United
7 States Patent No. 10,134,967 entitled “Light Emitting Device” (“the ’967 patent”), including the
8 right to sue and to recover for infringement thereof. The ’967 patent was duly and legally issued on
9 November 20, 2018, by the United States Patent and Trademark Office to Seo at al. A copy of the
10 ’967 patent is attached hereto as Exhibit 13.

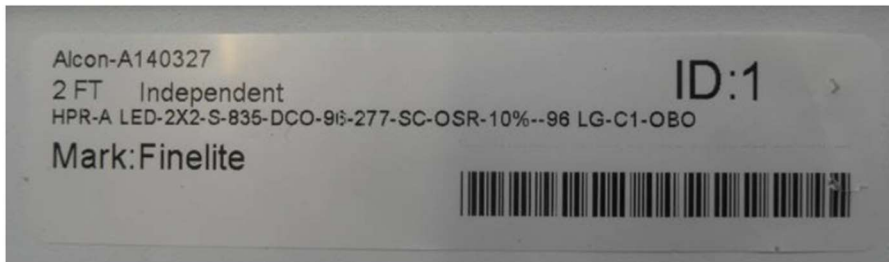
11 24. Seoul Semiconductor is the lawful owner of all right, title, and interest in United
12 States Patent No. 10,510,933 entitled “Light Emitting Diode Package and Manufacturing Method
13 Thereof” (“the ’933 patent”), including the right to sue and to recover for infringement thereof. The
14 ’933 patent was duly and legally issued on December 17, 2019, by the United States Patent and
15 Trademark Office to Oh et al. A copy of the ’933 patent is attached hereto as Exhibit 14.

16 **WILLFULNESS**

17 25. On March 24, 2020, Seoul Semiconductor sent a letter to Finelite’s Chief Executive
18 Officer notifying him that Finelite was selling products that were infringing Seoul Semiconductor’s
19 patents. Seoul Semiconductor specifically pointed out that the below identified product was
20 infringing at least the ’210 patent, ’157 patent, and ’514 patent. Seoul Semiconductor also urged
21 Finelite to “review SSC’s LED related patents carefully, because unauthorized use of SSC’s patents
22 can constitute direct, contributory and/or induced patent infringement . . .” Seoul Semiconductor
23 asked that Finelite confirm in writing that it would stop selling products that infringe Seoul
24 Semiconductor’s patent rights.

25 26. On April 6, 2020, Finelite’s corporate attorney responded and said that he was “not
26 clear” how the product “relates to Finelite.” He asked Seoul Semiconductor to provide detailed
27 claim charts for each claim of each patent.
28

27. On April 20, 2020, Seoul Semiconductor provided Finelite with pictures of the packaging for the product and an invoice specifically showing that the product appeared to be manufactured and supplied in the United States by Finelite. Below are pictures provided by Seoul Semiconductor to Finelite. In addition, Seoul Semiconductor pointed out that it had already provided Finelite with specific patents and the product that was infringing and asked again that Finelite confirm that it would stop selling products infringing Seoul Semiconductor’s patents. Seoul Semiconductor did not receive any response to this letter.



Line	Ordered	Shipped	Part No & Description	Unit Cost	Amount
1	1		<u>FINELITE</u> <u>HPR LED - A - 2x2 - DCO - S - 835 - 277V - SC - C1 - OBO</u> LED 2X2 TROFFER ANGLED DOOR STYLE DIFFUSE CENTER OPTIC STANDARD OUTPUT 29W 3397LUMS 3500K 277V SINGLE CIRCUIT FOR T-BAR INSTALLATION INCLUDES OCCUPANCY SENSOR	484.69	484.69T

28. On May 28, 2020, having received no response from Finelite, Seoul Semiconductor sent another letter to Finelite, reminding both its corporate counsel and its Senior Vice President and General Counsel, of its prior warning letters. Seoul Semiconductor again asked Finelite to stop

1 selling products that were infringing Seoul Semiconductor’s patents.

2 29. On June 1, 2020, Finelite sent a response to Seoul Semiconductor, refusing to stop
3 selling the products. Finelite stated that it “rejects” the “significant self-disruption of its business”
4 based on Seoul Semiconductor’s patent infringement allegations. Finelite also stated that it may not
5 infringe patents because it was not the maker of certain LED components that it incorporates in its
6 products.

7 30. On June 19, 2020, Seoul Semiconductor responded to Finelite and explained that
8 U.S. patent laws hold someone responsible for infringing when it sells products that infringe
9 someone else’s patent, even if “that company is not the end maker.” Seoul Semiconductor offered
10 to provide claim charts to Finelite in exchange for Finelite providing the names of its suppliers that
11 Finelite contends were manufacturing the infringing components. Seoul Semiconductor did not
12 receive any response to this letter.

13 31. On July 29, 2020, having received no response, Seoul Semiconductor again
14 contacted Finelite and agreed to provide claim charts subject to a non-disclosure agreement and
15 Finelite’s provision of supplier information so that the parties could resolve the matter amicably.
16 Seoul Semiconductor did not receive any response to this letter.

17 32. On September 2, 2020, Seoul Semiconductor sent another reminder letter to Finelite
18 and requested again that Finelite stop selling products that infringe Seoul Semiconductor’s patents.

19 33. On September 15, 2020, Finelite sent a letter refusing to provide any supplier
20 information, claiming that it had “potential confidentiality obligations” that would prevent it from
21 disclosing any such information.

22 34. On September 17, 2020, Seoul Semiconductor sent a letter to Finelite attaching a
23 draft non-disclosure agreement and requesting again that it provide supplier information in
24 exchange for claim charts, given that Seoul Semiconductor was under no legal obligation to provide
25 claim charts to Finelite, since Finelite could conduct its own infringement analysis with the
26 information that Seoul Semiconductor had already provided to Finelite.

27 35. On October 2, 2020, Finelite continued to refuse to provide any supplier
28 information, claiming it was “sensitive, business-related information,” despite Seoul

1 Semiconductor's agreement to make such information subject to a non-disclosure confidentiality
2 agreement ("NDA").

3 36. On November 19, 2020, Seoul Semiconductor sent Finelite another letter because
4 Seoul Semiconductor had still not received a signed NDA from Finelite. Seoul Semiconductor
5 noted that it had now provided notice to Finelite for more than six months of patent infringement,
6 and yet it appeared that Finelite was not only continuing to sell the infringing products, but had not
7 conducted any infringement analysis of its own related to Seoul Semiconductor's patents.

8 37. On December 10, 2020, Finelite signed the NDA, and on December 14, 2020, Seoul
9 Semiconductor promptly sent it an eight-page claim chart showing an example of its product
10 infringing Seoul Semiconductor's patents. The claim chart showed cross-section images from a
11 transmission electron microscope of Finelite's product, as well as secondary ion mass spectrometry
12 data for the epitaxial structure of the LED chip contained in Finelite's product.

13 38. On February 2, 2021, Seoul Semiconductor wrote again to Finelite to follow up and
14 request again that Finelite would stop selling products that infringe Seoul Semiconductor's patents.

15 39. On February 15, 2021, Finelite wrote back and claimed that the information
16 provided in the claim charts still was not enough for Finelite to "meaningfully assess" whether its
17 product was infringing Seoul Semiconductor's patents.

18 40. On March 8, 2021, Seoul Semiconductor sent another letter to Finelite and noted
19 that Finelite had still not stopped selling the products and appeared to have taken no steps of its
20 own to conduct an infringement analysis or provide any supplier information. Seoul Semiconductor
21 stated "It is clear to us that no matter what information we provided you, you would claim that it
22 was somehow 'unclear' as an excuse to continue selling products that infringe SSC's patents."
23 Seoul Semiconductor again requested that Finelite stop selling products that infringe Seoul
24 Semiconductor's patents. Finelite did not respond to this letter.

25 41. On April 21, 2021, Seoul Semiconductor sent another letter to Finelite, again
26 requesting that it stop selling products that infringe Seoul Semiconductor's patents. Seoul
27 Semiconductor stated that turning "a blind eye to infringement and feigning ignorance will not
28 resolve this matter" and warned that continuing to infringe Seoul Semiconductor's patents could

1 constitute willful infringement in a patent litigation. Finelite did not respond to this letter.

2 42. On May 25, 2021, Seoul Semiconductor sent another letter to Finelite, again
3 requesting that it stop selling products that infringe Seoul Semiconductor’s patents.

4 43. On June 3, 2021, Finelite sent a letter to Seoul Semiconductor demanding that it
5 respond to various litigation-style questions to “assess the substance” of Seoul Semiconductor’s
6 infringement allegations. Finelite repeated that it was unwilling to “self-disrupt its business” and
7 stop selling products that Seoul Semiconductor had told it were infringing Seoul’s patents. Finelite
8 did not, to Seoul Semiconductor’s information and belief, ever stop selling the products that Seoul
9 Semiconductor had identified numerous times as infringing its patents.

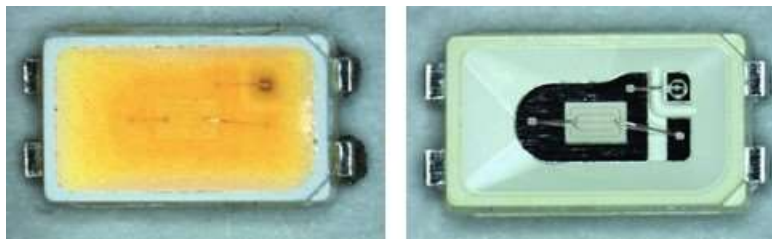
10 **COUNT 1**

11 **INFRINGEMENT OF U.S. PATENT NO. 7,397,069**

12 **EXEMPLARY CLAIM 1**

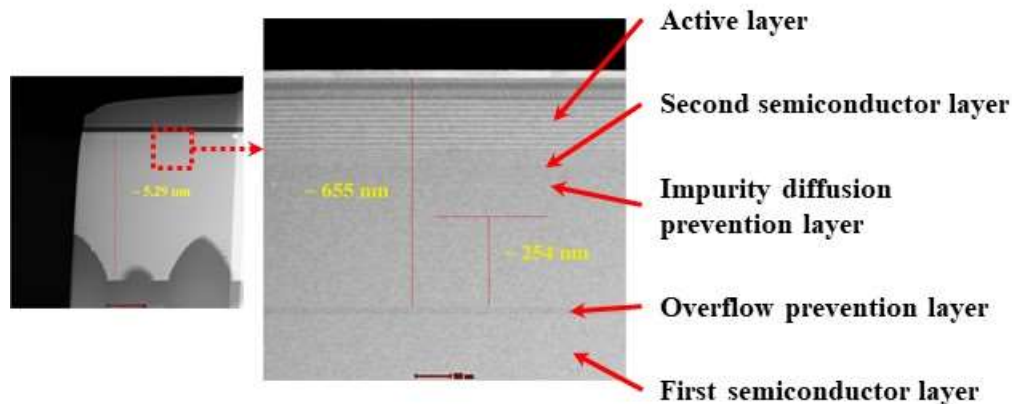
13 44. Finelite has infringed and continues to infringe one or more claims of the ’069
14 patent, including but not limited to exemplary claim 1, in violation of 35 U.S.C. § 271(a), at least
15 by without authority making, using, offering to sell and/or selling the Finelite HPR-A-2X2-S-835-
16 DCO-LED-3500K-277V-SC-C1-OBO LED panel within the United States.

17 45. The Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel
18 includes a plurality of LED packages. Optical microscope images of an LED package from the
19 Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel are reproduced
20 below before and after removal of an encapsulant.



25 46. The below images were created using a transmission electron microscope (TEM) of
26 a semiconductor device (light emitting diode) after removal from the package. The TEM images
27 indicate the presence of a plurality of gallium-nitride-based layers. Those layers include, in relevant
28 part, from top to bottom: an active layer comprising a multi-quantum well, a second semiconductor

1 layer doped with the negative dopant silicon, an impurity diffusion prevention layer having a
 2 relatively high indium concentration and relatively low concentration of aluminum, an overflow
 3 prevention layer having the negative dopant silicon and a relatively high concentration of aluminum,
 4 and first semiconductor layer containing the negative dopant silicon. The indium and aluminum
 5 concentrations of the impurity prevention layer, which respectively correlate directly and indirectly
 6 with the brightness of the layers, indicate a band gap smaller than those of the overflow prevention
 7 layer, the first semiconductor layer and the second semiconductor layer.



15 47. Finelite’s infringement has caused and is continuing to cause damage and
 16 irreparable injury to Plaintiffs. Plaintiffs will continue to suffer damage and irreparable injury
 17 unless and until that infringement is enjoined by this Court, as a remedy at law alone would be
 18 inadequate.

19 48. Plaintiffs are entitled to injunctive relief and damages in accordance with 35 U.S.C.
 20 §§ 271, 281, 283, and 284.

21 **COUNT 2**

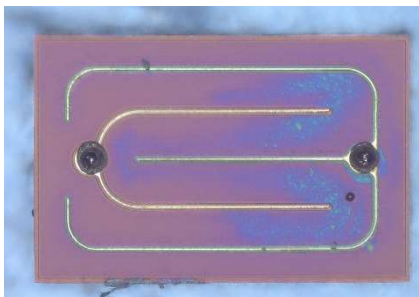
22 **INFRINGEMENT OF U.S. PATENT NO. 9,269,868**

23 **EXEMPLARY CLAIM 1**

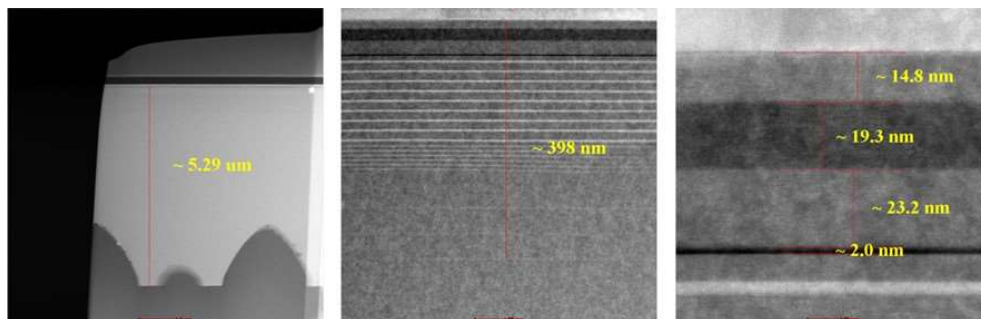
24 49. Finelite has infringed and continues to infringe one or more claims of the ’868
 25 patent, including but not limited to exemplary claim 1, in violation of 35 U.S.C. § 271(a), at least
 26 by without authority making, using, offering to sell and/or selling the Finelite HPR-A-2X2-S-835-
 27 DCO-LED-3500K-277V-SC-C1-OBO LED panel within the United States.

28 50. The Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel

1 includes a plurality of semiconductor light emitting elements. An optical image of an example light
2 emitting element is reproduced below.



8 51. Three TEM images of the light emitting element are reproduced below. The image
9 below left shows the full epitaxial structure above a patterned sapphire substrate. The center image
10 indicates a plurality of layers including from bottom to top an n-type semiconductor layer, a light
11 emitting unit, and a p-type semiconductor layer. The image below right focusses in on the p-type
12 semiconductor layer. As the image shows, a number of layers as described above are located below
13 the p-type semiconductor layer, with the relative brightness of each layer correlating with the
14 dopant concentration. From bottom to top, the layers include a first layer, a second layer, and a
15 third layer, each of which has different levels of the magnesium doping, the first layer with
16 relatively low magnesium doping, the second layer with relatively high magnesium doping, and the
17 third layer with relatively intermediate magnesium doping.



24 52. Finelite's infringement has caused and is continuing to cause damage and
25 irreparable injury to Plaintiffs. Plaintiffs will continue to suffer damage and irreparable injury
26 unless and until that infringement is enjoined by this Court, as a remedy at law alone would be
27 inadequate.
28

1 53. Plaintiffs are entitled to injunctive relief and damages in accordance with 35 U.S.C.
2 §§ 271, 281, 283, and 284.

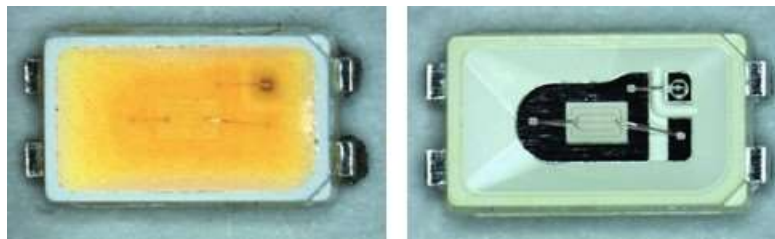
3 **COUNT 3**

4 **INFRINGEMENT OF U.S. PATENT NO. 9,799,800**

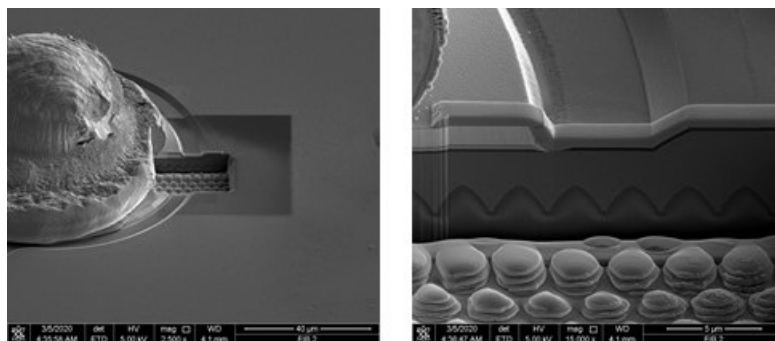
5 **EXEMPLARY CLAIM 1**

6 54. Finelite has infringed and continues to infringe one or more claims of the '800
7 patent, including but not limited to exemplary claim 1, in violation of 35 U.S.C. § 271(a), at least
8 by without authority making, using, offering to sell and/or selling the Finelite HPR-A-2X2-S-835-
9 DCO-LED-3500K-277V-SC-C1-OBO LED panel within the United States.

10 55. The Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel
11 includes a plurality of LED packages, each of which comprises a light emitting diode. Optical
12 microscope images of the LED package from the Finelite HPR-A-2X2-S-835-DCO-LED-3500K-
13 277V-SC-C1-OBO LED panel are reproduced below before and after removal of an encapsulant.
14 As shown in the image below right, the LED package includes a light emitting diode (LED) chip.

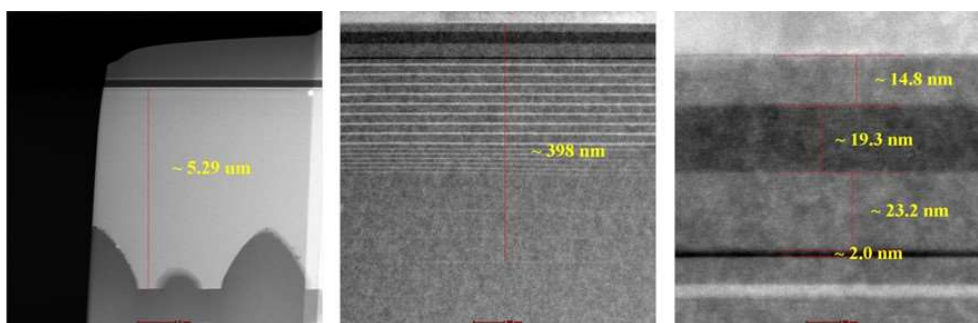


19 56. Below are two Scanning Electron Microscope (SEM) images of an LED chip from
20 the Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel. The images
21 were created after the creation of a hole in the LED using a focused ion beam (FIB). The structure
22 shown on the left side of the first image is an n-pad, which sits on top of and in contact with an n-
23 type semiconductor layer. Above the n-type semiconductor layer are an active layer, an electron
24 blocking layer and a p-type semiconductor layer. The active layer comprises a multi-quantum well,
25 with the layers distinguishable by differing indium concentrations. Between the active layer and
26 the p-type layer is an electron blocking layer, which is distinguishable by being relatively free of
27 both magnesium and indium.
28



57. The p-type semiconductor layer of the LED chip comprises a number of layers. From top to bottom, the layers include a p-type contact layer, a hole transport layer, and a hole injection layer. The p-type contact layer is distinguishable by a relatively high level of magnesium doping near the surface of the LED chip. Next, a hole transport layer has varying levels of the magnesium doping, a first layer with relatively low magnesium doping, an intermediate layer with relatively high magnesium doping, and second layer with relatively low magnesium doping. The level of magnesium within the first layer with relatively low level doping increases at both ends and decreases toward the middle. The hole injection layer is again distinguishable based on its level of magnesium doping.

58. Three transmission electron microscope images of the LED chip are reproduced below. The image below left shows the full epi-structure above a patterned sapphire substrate. The center image indicates a plurality of layers including from bottom to top an n-type semiconductor layer, a multi-quantum well active layer, and a p-type semiconductor layer. The image below right focusses in on the p-type semiconductor layer and a portion of the multi-quantum well active layer. As the image shows, the p-type semiconductor layer includes a number of layers as described above, with the relative brightness of each layer correlating with the dopant concentration.



1 59. Finelite’s infringement has caused and is continuing to cause damage and
2 irreparable injury to Plaintiffs. Plaintiffs will continue to suffer damage and irreparable injury
3 unless and until that infringement is enjoined by this Court, as a remedy at law alone would be
4 inadequate.

5 60. Plaintiffs are entitled to injunctive relief and damages in accordance with 35 U.S.C.
6 §§ 271, 281, 283, and 284.

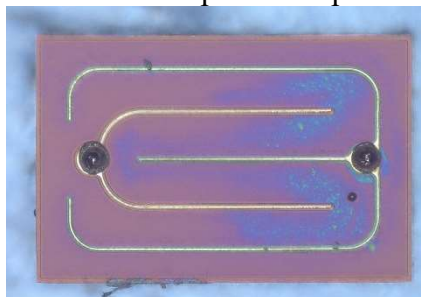
7 **COUNT 4**

8 **INFRINGEMENT OF U.S. PATENT NO. 7,667,225**

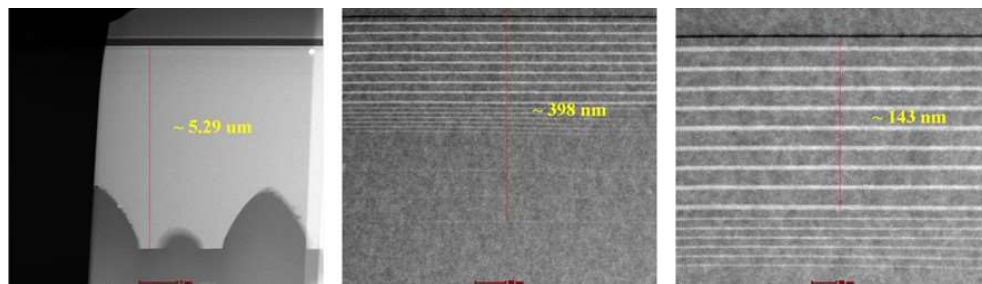
9 **EXEMPLARY CLAIM 1**

10 61. Finelite has infringed and continues to infringe one or more claims of the ’225
11 patent, including but not limited to exemplary claim 1, in violation of 35 U.S.C. § 271(a), at least
12 by without authority making, using, offering to sell and/or selling the Finelite HPR-A-2X2-S-835-
13 DCO-LED-3500K-277V-SC-C1-OBO LED panel within the United States.

14 62. The Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel
15 includes a plurality of LED chips. An optical image of an LED chip from the Finelite HPR-A-2X2-
16 S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel is reproduced below.



21 63. Below are three TEM images of the epitaxial structure of the LED chip. The image
22 to the left shows the entire epitaxial structure above a patterned sapphire substrate. The image below
23 middle enlarges the region of the epitaxial structure around the multi-quantum well active layer.
24 The image below right enlarges the multi-quantum well. The LED chip includes from bottom to
25 top in relevant part a substrate, an n-type semiconductor layer, a multi-quantum well structure, and
26 a p-type semiconductor layer. The multi-quantum well structure comprises brightly colored wells
27 separated by darker barriers.
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6 64. The well layers within the multi-quantum well include indium. In addition, the
7 concentration of indium varies across the layer, with areas of relatively high indium
8 concentration transitioning to areas of lower indium concentration. The regions of relatively higher indium
9 concentration correspond to carrier trap portions. And the transition from relatively lower to
10 relatively higher indium concentration corresponds to a related drop in the band-gap energy.

11 65. Finelite's infringement has caused and is continuing to cause damage and
12 irreparable injury to Plaintiffs. Plaintiffs will continue to suffer damage and irreparable injury
13 unless and until that infringement is enjoined by this Court, as a remedy at law alone would be
14 inadequate.

15 66. Plaintiffs are entitled to injunctive relief and damages in accordance with 35 U.S.C.
16 §§ 271, 281, 283, and 284.

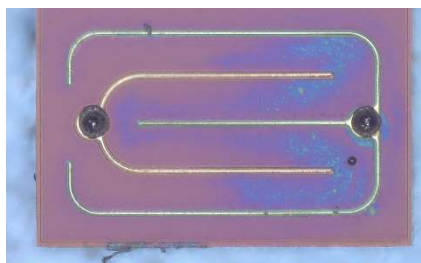
17 **COUNT 5**

18 **INFRINGEMENT OF U.S. PATENT NO. 8,664,638**

19 **EXEMPLARY CLAIM 1**

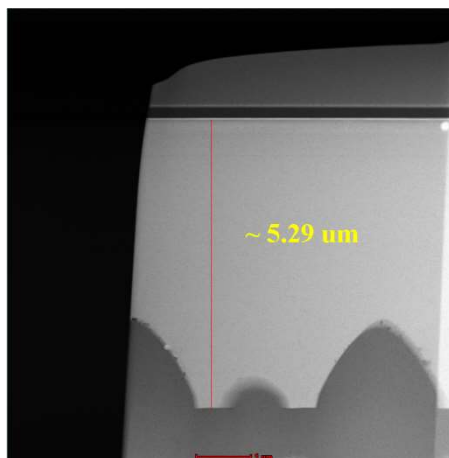
20 67. Finelite has infringed and continues to infringe one or more claims of the '638
21 patent, including but not limited to exemplary claim 1, in violation of 35 U.S.C. § 271(a), at least
22 by without authority making, using, offering to sell and/or selling the Finelite HPR-A-2X2-S-835-
23 DCO-LED-3500K-277V-SC-C1-OBO LED panel within the United States.

24 68. The Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel
25 includes a plurality of LED chips. An optical image of an LED chip from the Finelite HPR-A-2X2-
26 S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel is reproduced below.



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69. The above image depicts two pads. The right side of the chip includes a p-pad, which
6 is electrically connected to a p-type contact layer. The left side of the chip includes an n-pad, which
7 is electrically connected to an n-type contact layer.
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70. A TEM image depicting the cross section of the epitaxial layers of the chip from the
10 Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel is reproduced
11 below.



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71. The chip from the Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-
20 OBO LED panel includes a plurality of different semiconductor layers formed over a substrate.
21 Among the layers included are, from bottom to top, a buffer layer, a gallium nitride-based first
22 lower semiconductor layer, a gallium nitride-based first interlayer that comprises a single
23 composition, a gallium nitride-based second interlayer, a gallium nitride-based n-type contact layer,
24 an active layer, and a gallium nitride-based p-type contact layer. The first interlayer comprises the
25 same composition as the n-type contact layer.

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72. Upon information and belief, the second interlayer has a higher dislocation density
than the first lower semiconductor layer and the second interlayer has a dislocation density different
from that of the first interlayer. Upon information and belief, the first interlayer has a lower

1 dislocation density than the buffer layer, and has higher dislocation density than the first lower
2 semiconductor layer.

3 73. Finelite's infringement has caused and is continuing to cause damage and
4 irreparable injury to Plaintiffs. Plaintiffs will continue to suffer damage and irreparable injury
5 unless and until that infringement is enjoined by this Court, as a remedy at law alone would be
6 inadequate.

7 74. Plaintiffs are entitled to injunctive relief and damages in accordance with 35 U.S.C.
8 §§ 271, 281, 283, and 284.

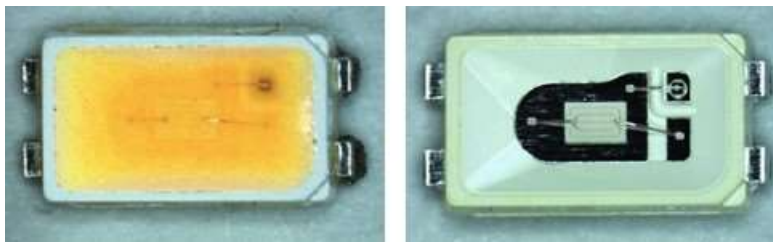
9 **COUNT 6**

10 **INFRINGEMENT OF U.S. PATENT NO. 9,716,210**

11 **EXEMPLARY CLAIM 1**

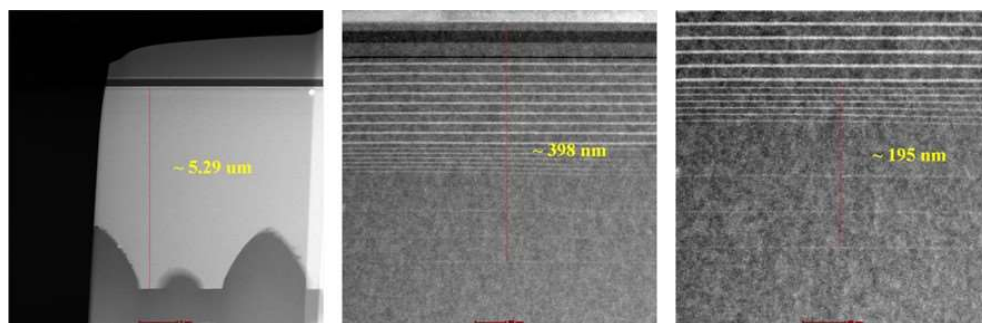
12 75. Finelite has infringed and continues to infringe one or more claims of the '210
13 patent, including but not limited to exemplary claim 1, in violation of 35 U.S.C. § 271(a), at least
14 by without authority making, using, offering to sell and/or selling the Finelite HPR-A-2X2-S-835-
15 DCO-LED-3500K-277V-SC-C1-OBO LED panel within the United States.

16 76. The Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel
17 includes a plurality of LED packages, each of which includes a light emitting diode. The image of
18 an LED package from the Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED
19 panel is reproduced below left. The image below right shows an LED chip within the package.



24 77. Below are three TEM images of the epi-structure of the LED chip. The image to the
25 left shows the entire epi-structure above a patterned sapphire substrate. The image below middle
26 enlarges the region of the epi-structure around the multi-quantum well active layer. The image
27 below right further enlarges the region under the active region. The epi-structure includes from top
28 to bottom in relevant part a p-type contact layer, a multi-quantum well active region, and an n-type

1 contact layer. The active region, which appears as a relatively bright repeating pattern of indium
 2 doped layers separated by relatively dark barrier layers near the center of the center image below.
 3 Below the active region is a superlattice layer, which includes a plurality of layers, and appears as
 4 a relatively faint and closely spaced pattern. Below the superlattice is a spacer layer, which includes
 5 a plurality of layers, and appears as a relatively faint and widely spaced pattern.



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 11 78. Based on the level of indium doping in the active region, the superlattice layer, and
 12 the spacer layer, the spacer layer will have a bandgap smaller than the barrier layers of the multi-
 13 quantum well, but higher than the bandgap of the quantum well layers.

14 79. Finelite's infringement has caused and is continuing to cause damage and
 15 irreparable injury to Plaintiffs. Plaintiffs will continue to suffer damage and irreparable injury
 16 unless and until that infringement is enjoined by this Court, as a remedy at law alone would be
 17 inadequate.

18 80. Finelite's infringement has occurred with knowledge of the '210 patent and
 19 knowledge that its acts constitute infringement. Finelite's continuing conduct, therefore, is willful.

20 81. Plaintiffs are entitled to injunctive relief and damages in accordance with 35 U.S.C.
 21 §§ 271, 281, 283, and 284.

22 COUNT 7

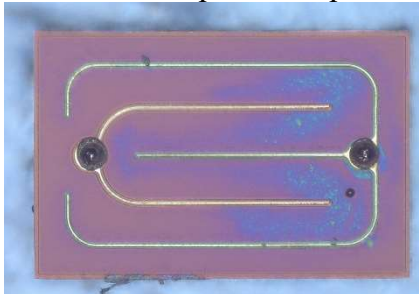
23 **INFRINGEMENT OF U.S. PATENT NO. 10,418,514**

24 **EXEMPLARY CLAIM 1**

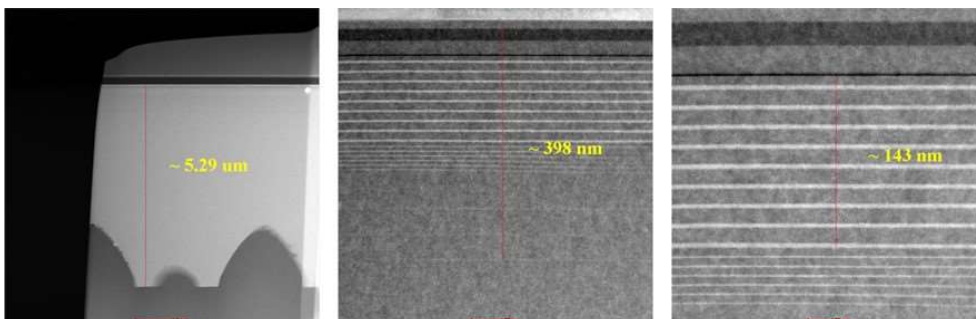
25 82. Finelite has infringed and continues to infringe one or more claims of the '514
 26 patent, including but not limited to exemplary claim 1, in violation of 35 U.S.C. § 271(a), at least
 27 by without authority making, using, offering to sell and/or selling the Finelite HPR-A-2X2-S-835-
 28

1 DCO-LED-3500K-277V-SC-C1-OBO LED panel within the United States.

2 83. The Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel
 3 includes a plurality of LED chips. An optical image of an LED chip from the Finelite HPR-A-2X2-
 4 S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel is reproduced below.



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 10 84. The images below are TEM images created from a slice of the lighting diode. The
 11 image below left includes the entire epitaxial structure above the substrate. The image below center
 12 enlarges the region above and below the active layer. The region below the active layer includes an
 13 n-type contact layer, the region above the active layer includes a p-type contact layer. The image
 14 below right enlarges the active layer, showing relatively thin and bright multi-quantum well layers.



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 21 85. A number of sublayers are depicted in the image above center both above and below
 22 the active layer. Those layers include a p-type clad layer, a superlattice layer that includes a plurality
 23 of layers disposed near active region, and a spacer layer between the superlattice layer and the n-
 24 type contact layer. The spacer layer has a bandgap smaller than the barrier layer and greater than
 25 the quantum well layer.

26 86. Finelite's infringement has caused and is continuing to cause damage and
 27 irreparable injury to Plaintiffs. Plaintiffs will continue to suffer damage and irreparable injury
 28 unless and until that infringement is enjoined by this Court, as a remedy at law alone would be

1 inadequate.

2 87. Finelite's infringement has occurred with knowledge of the '514 patent and
3 knowledge that its acts constitute infringement. Finelite's continuing conduct, therefore, is willful.

4 88. Plaintiffs are entitled to injunctive relief and damages in accordance with 35 U.S.C.
5 §§ 271, 281, 283, and 284.

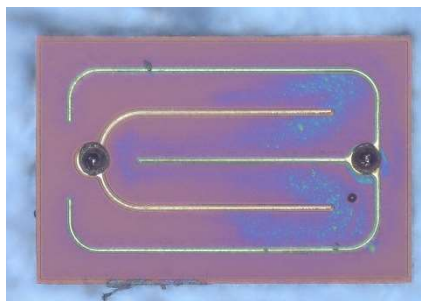
6 **COUNT 8**

7 **INFRINGEMENT OF U.S. PATENT NO. 8,604,496**

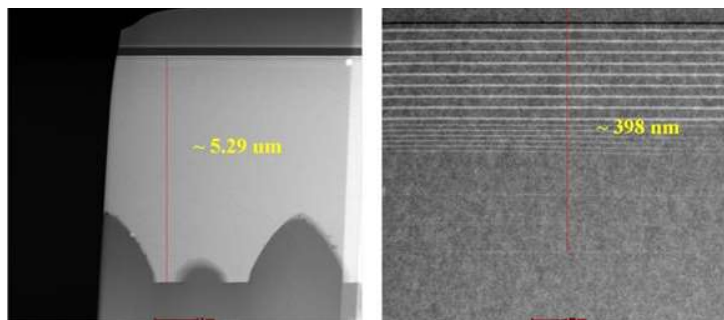
8 **EXEMPLARY CLAIM 1**

9 89. Finelite has infringed and continues to infringe one or more claims of the '496
10 patent, including but not limited to exemplary claim 1, in violation of 35 U.S.C. § 271(a), at least
11 by without authority making, using, offering to sell and/or selling the Finelite HPR-A-2X2-S-835-
12 DCO-LED-3500K-277V-SC-C1-OBO LED panel within the United States.

13 90. The Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel
14 includes a plurality of LED chips. An optical image of an LED chip from the Finelite HPR-A-2X2-
15 S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel is reproduced below.

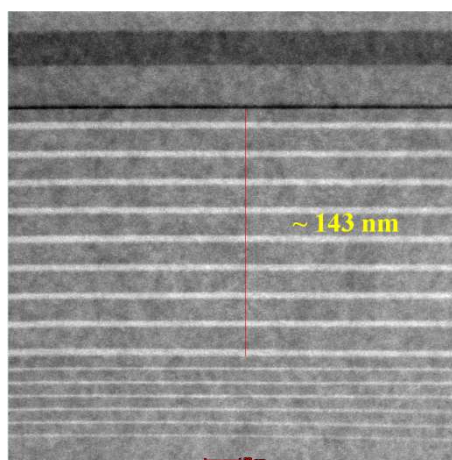


21 91. The images below are two TEM images created from a thin section taken from the
22 LED chip. The image below left shows a substrate toward the bottom of the image. The image
23 below right is a higher resolution image focused on the light emitting layer formed above the
24 substrate.



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92. The layer structure in the image above right includes, in relevant part from bottom to top, an n-type semiconductor layer, a functional part, and a p-type semiconductor layer.

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93. The image below is a TEM image focused on the functional part, which includes a plurality of active layers stacked in a direction from the lower n-type semiconductor layer toward the upper p-type semiconductor layer.



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94. The above depicted functional part includes multiple active layers that include a multilayer stacked body including a plurality of thick film layers and a plurality of thin film layers alternately stacked in the direction, a thickness of the thin film layers being not more than a thickness of the thick film layers. The thin film layers appear as relatively bright and thin layers (wells) separated by relatively thick dimmer layers (barriers). A multilayer stacked body can include, for example, a pair of wells and three surrounding barrier layers.

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95. Within the overall structure, the active layer includes an n-side barrier layer provided between the multilayer stacked body and the p-type semiconductor layer, a well layer; and a p-side barrier layer provided between the well layer and the p-type semiconductor layer. With

1 that arrangement, the well layer is provided between the n-side barrier layer and the p-side barrier
2 layer.

3 96. Finelite's infringement has caused and is continuing to cause damage and
4 irreparable injury to Plaintiffs. Plaintiffs will continue to suffer damage and irreparable injury
5 unless and until that infringement is enjoined by this Court, as a remedy at law alone would be
6 inadequate.

7 97. Plaintiffs are entitled to injunctive relief and damages in accordance with 35 U.S.C.
8 §§ 271, 281, 283, and 284.

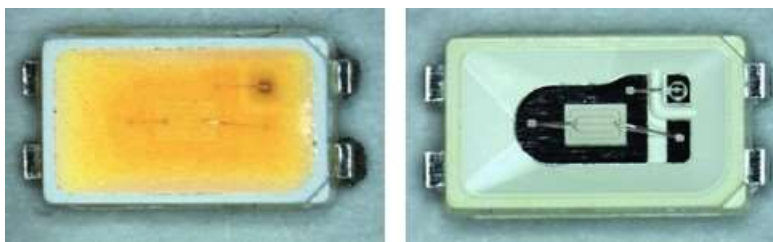
9 **COUNT 9**

10 **INFRINGEMENT OF U.S. PATENT NO. 7,982,207**

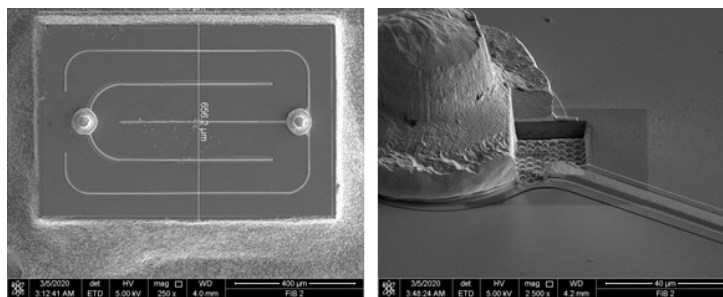
11 **EXEMPLARY CLAIM 7**

12 98. Finelite has infringed and continues to infringe one or more claims of the '207
13 patent, including but not limited to exemplary claim 7, in violation of 35 U.S.C. § 271(a), at least
14 by without authority making, using, offering to sell and/or selling the Finelite HPR-A-2X2-S-835-
15 DCO-LED-3500K-277V-SC-C1-OBO LED panel within the United States.

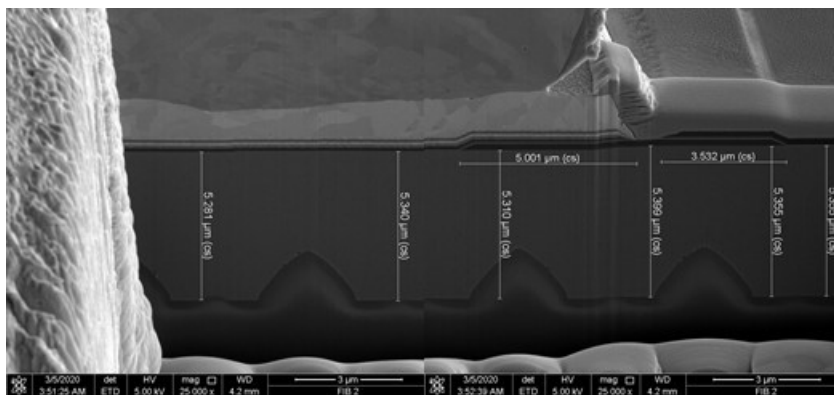
16 99. The Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel
17 includes a plurality of LED packages, each of which includes a light emitting diode. The image of
18 an LED package from the Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED
19 panel is reproduced below left. The image below right shows the LED chip within the package.



24 100. Below are two SEM images of the LED chip. The image to the left shows the top
25 image of the LED chip, and the image below right shows the area surrounding the p-electrode of
26 the LED chip. The dark space near the p-electrode indicates a hole created using a FIB.
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101. The below composite image shows the cross-section of the hole created by the FIB. Focusing in on the central part of the image, the cross-section shows from bottom to top (in relevant part) a substrate, an n-type semiconductor layer, an active layer, a p-type semiconductor layer, a transparent electrode layer and the p-electrode pad, which includes a thin layer of aluminum on its bottom surface. The transparent electrode layer is comprised of indium tin oxide, which appears as a relatively thin lightly colored layer in contact with the top surface of the p-type semiconductor layer at the far right side of the image.



102. Focusing on the left half of the image above, there is shown from bottom to top a substrate, an n-type semiconductor layer, an active layer, a p-type semiconductor layer, and the p-electrode pad, which includes a thin layer of aluminum on its bottom surface. More specifically, in the left region the thin layer of aluminum contacts the underlying p-type semiconductor rather than the transparent electrode layer. That area of contact indicates an opening in the transparent electrode layer, where the contact between the aluminum and p-type semiconductor layer, comprises a current blocking portion.

103. With respect the central region of the image above, the electrode pad is arranged on an upper surface of the transparent electrode layer outside of the opening.

1 104. Finelite's infringement has caused and is continuing to cause damage and
2 irreparable injury to Plaintiffs. Plaintiffs will continue to suffer damage and irreparable injury
3 unless and until that infringement is enjoined by this Court, as a remedy at law alone would be
4 inadequate.

5 105. Plaintiffs are entitled to injunctive relief and damages in accordance with 35 U.S.C.
6 §§ 271, 281, 283, and 284.

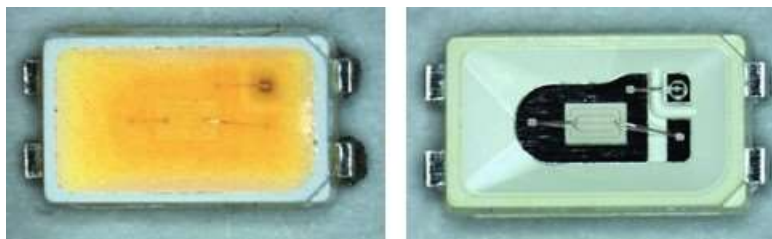
7 **COUNT 10**

8 **INFRINGEMENT OF U.S. PATENT NO. 10,672,952**

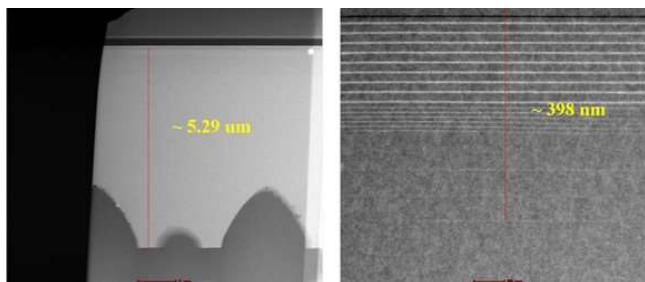
9 **EXEMPLARY CLAIM 1**

10 106. Finelite has infringed and continues to infringe one or more claims of the '952
11 patent, including but not limited to exemplary claim 1, in violation of 35 U.S.C. § 271(a), at least
12 by without authority making, using, offering to sell and/or selling the Finelite HPR-A-2X2-S-835-
13 DCO-LED-3500K-277V-SC-C1-OBO LED panel within the United States.

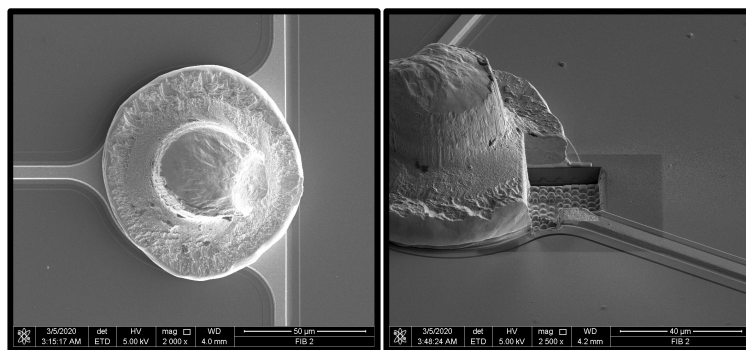
14 107. The Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel
15 includes a plurality of LED packages. Optical microscope images of an LED package from the
16 Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel are reproduced
17 below before and after removal of an encapsulant. The image below right shows a light emitting
18 diode at the center of the package.



23 108. Two TEM images of the light emitting element are reproduced below. The image
24 below left shows the full epitaxial structure above a patterned sapphire substrate. The image below
25 right indicates a plurality of layers including from bottom to top an n-type semiconductor layer, an
26 active layer, and a p-type semiconductor layer. The active layer is disposed between the n-type
27 semiconductor layer and the p-type semiconductor layer and is operable to emit light in response
28 to an electrical current through the light emitting structure.

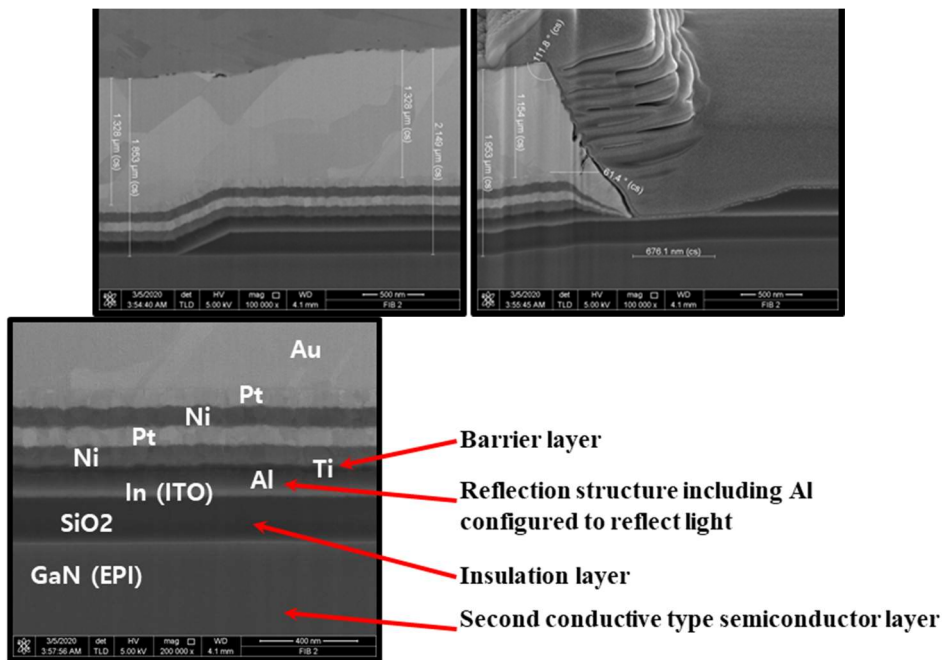


109. Below are two Scanning Electron Microscope (SEM) images of an LED chip from the Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel. The image on the left shows the p-pad, with three conductors extending from the p-pad. The image on the right was created after the creation of a hole in the LED using a focused ion beam (FIB).



110. The first two images below are SEM images showing the layer structure within the above FIB-created hole. The third image is an enlarged view of the layer structure above the semiconductor layers with an identification of the composition of the relevant layers. The LED includes an aluminum reflection structure disposed on the p-type semiconductor layer and is configured to reflect light emitted by the light emitting structure. The LED also includes an SiO_2 insulation layer disposed over the p-type semiconductor layer. The LED also includes a titanium barrier layer disposed over the second conductive type semiconductor layer and the reflection structure, the barrier layer structured to include conductive materials and to prevent a diffusion of the metal in the reflection structure.

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111. Finelite’s infringement has caused and is continuing to cause damage and irreparable injury to Plaintiffs. Plaintiffs will continue to suffer damage and irreparable injury unless and until that infringement is enjoined by this Court, as a remedy at law alone would be inadequate.

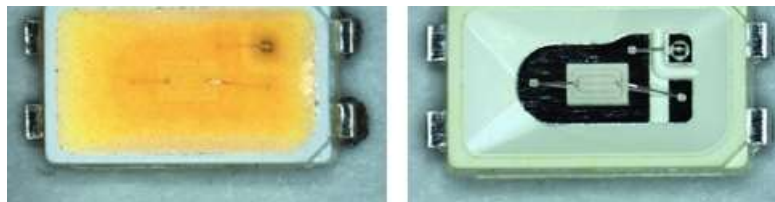
112. Plaintiffs are entitled to injunctive relief and damages in accordance with 35 U.S.C. §§ 271, 281, 283, and 284.

COUNT 11
INFRINGEMENT OF U.S. PATENT NO. 8,981,410
EXEMPLARY CLAIM 1

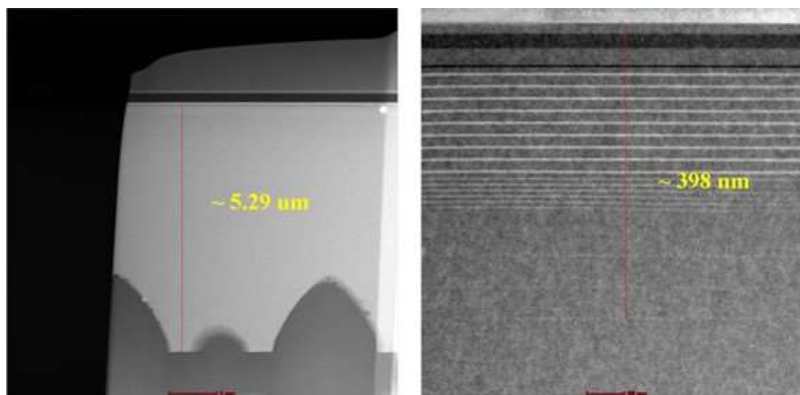
113. Finelite has infringed and continues to infringe one or more claims of the ’410 patent, including but not limited to exemplary claim 1, in violation of 35 U.S.C. § 271(a), at least by without authority making, using, offering to sell and/or selling the Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel within the United States.

114. The Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel includes a plurality of LED packages, each of which includes a light emitting diode. The image of an LED package from the Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel is reproduced below left. The image below right shows an LED chip within the package.

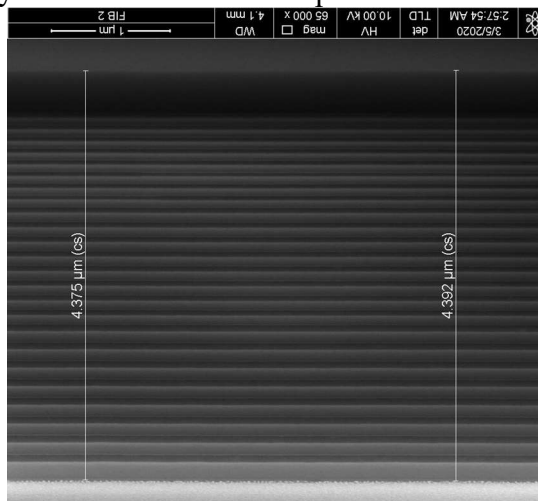
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115. The images below are two TEM images created from a thin section taken from the light emitting diode device. The image below left shows a substrate toward the bottom of the image. The image below right is a higher resolution image focused on the active layer formed above the substrate.



116. The image below is an SEM image created of a hole made in the light emitting diode device. The image is focused on a layer structure formed below the back surface of the substrate (which appears at the top of the image below). The layer structure shown in the image below includes, in relevant part starting from the top, the substrate, a low-index total internal reflection layer, and a distributed Bragg reflector (DBR). The low-index total internal reflection layer comprises a relatively thick layer of silicon dioxide disposed between the substrate and the DBR.



1 117. The DBR is comprised of interleaved layers of the high index dielectric titanium
2 dioxide and low index dielectric silicon dioxide. The titanium dioxide layers appear as relatively
3 light and thin layers relative to the silicon dioxide layers. As the image shows, the upper layers of
4 the DBR comprise a first plurality of periods where the high index material layers have a first
5 thickness and the low index materials have a second thickness. The image also shows that the lower
6 layers of the DBR comprise a second plurality of periods where the high index material layers have
7 a third thickness and the low index materials have a fourth thickness.

8 118. Finelite's infringement has caused and is continuing to cause damage and
9 irreparable injury to Plaintiffs. Plaintiffs will continue to suffer damage and irreparable injury
10 unless and until that infringement is enjoined by this Court, as a remedy at law alone would be
11 inadequate.

12 119. Plaintiffs are entitled to injunctive relief and damages in accordance with 35 U.S.C.
13 §§ 271, 281, 283, and 284.

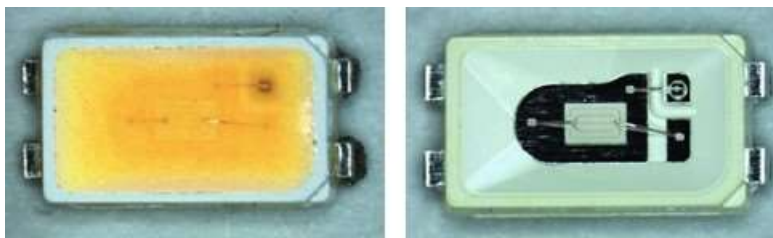
14 **COUNT 12**

15 **INFRINGEMENT OF U.S. PATENT NO. 9,577,157**

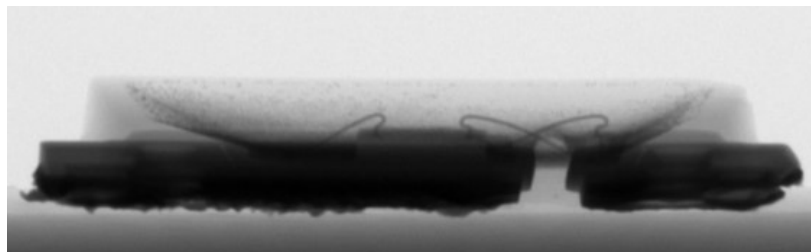
16 **EXEMPLARY CLAIM 1**

17 120. Finelite has infringed and continues to infringe one or more claims of the '157
18 patent, including but not limited to exemplary claim 1, in violation of 35 U.S.C. § 271(a), at least
19 by without authority making, using, offering to sell and/or selling the Finelite HPR-A-2X2-S-835-
20 DCO-LED-3500K-277V-SC-C1-OBO LED panel within the United States.

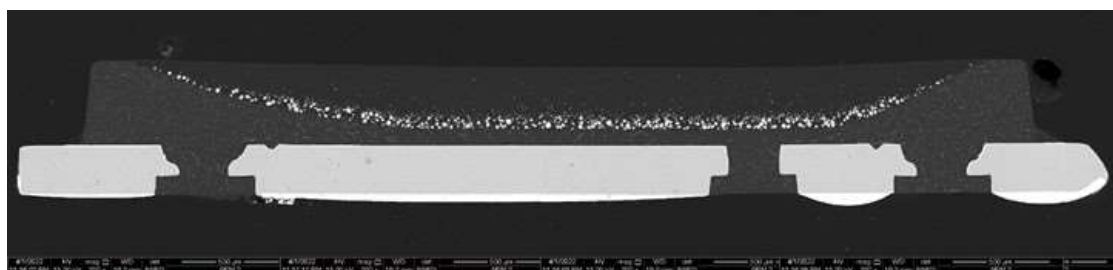
21 121. The Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel
22 includes a plurality of LED packages, each of which includes a light emitting diode. The image of
23 an LED package from the Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED
24 panel is reproduced below left. The image below right shows an LED chip within the package.



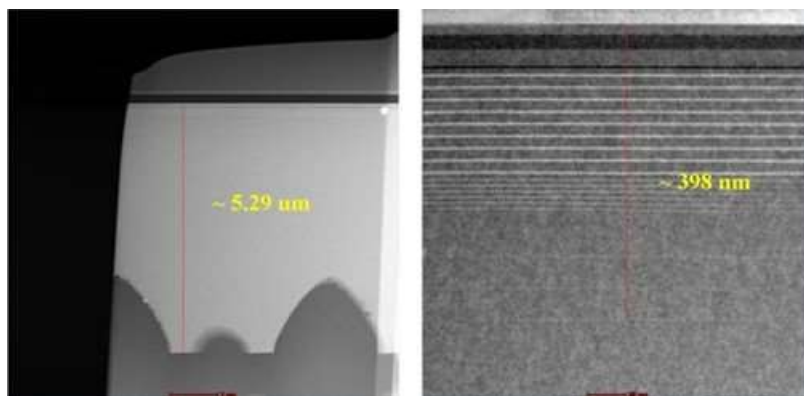
1 122. An X-ray side view image of the package is reproduced below. The image shows
 2 the body, leads, and a mounting surface upon which the LED chip is disposed. The LED chip is
 3 shown covered by a member comprised of resin.



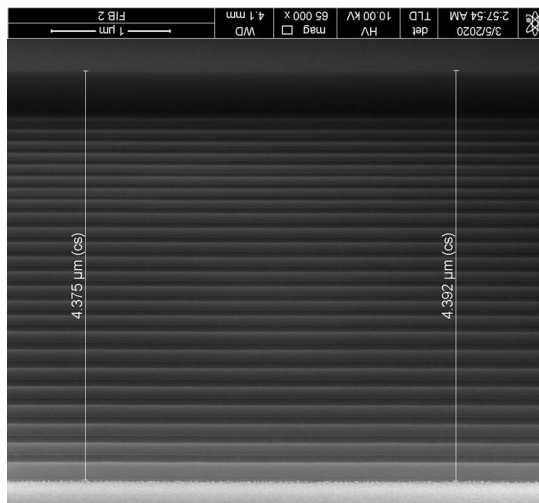
9 123. The image at bottom shows a SEM image of a cross-section through the package.
 10 The image shows the resin member contains phosphor particles, which can be seen as relatively
 11 bright spots within the darker resin.



17 124. Two transmission electron microscope images of a light emitting structure from the
 18 HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO are reproduced below. The image
 19 below left shows the entire vertical epi-structure formed above a patterned sapphire substrate. The
 20 image below right shows a portion of the epi-structure including from bottom to top, a portion of
 21 the n-type semiconductor layer, a multi-quantum well active layer, and a p-type semiconductor
 22 layer.



125. The image below is SEM image created of a hole made in the light emitting diode device. The image is focused on a layer structure formed below the back surface of the substrate (which appears at the top of the image below). The layer structure shown in the image below includes, in relevant part starting from the top, the substrate, and a DBR. As shown in the below image, the DBR comprises two portions., an upper portion comprising relatively thick layers of silicon dioxide (“SiO₂”) and titanium dioxide (“TiO₂”) and a lower portion comprising relatively thin layers of SiO₂ and TiO₂. The relatively dark layers comprise SiO₂ and the relatively bright layers comprise TiO₂. The optical thickness of the layers comprising the upper portion are greater than the optical thickness of the layers comprising the lower portion.



126. Finelite’s infringement has caused and is continuing to cause damage and irreparable injury to Plaintiffs. Plaintiffs will continue to suffer damage and irreparable injury unless and until that infringement is enjoined by this Court, as a remedy at law alone would be inadequate.

127. Finelite’s infringement has occurred with knowledge of the ’210 patent and knowledge that its acts constitute infringement. Finelite’s continuing conduct, therefore, is willful.

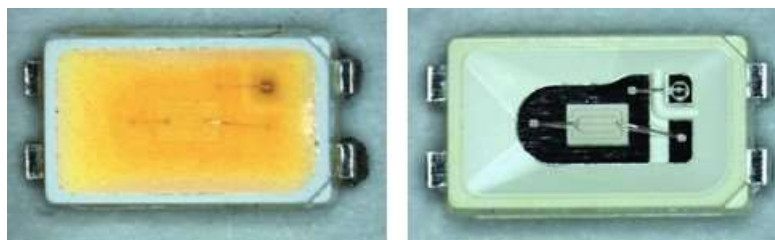
128. Plaintiffs are entitled to injunctive relief and damages in accordance with 35 U.S.C. §§ 271, 281, 283, and 284.

COUNT 13

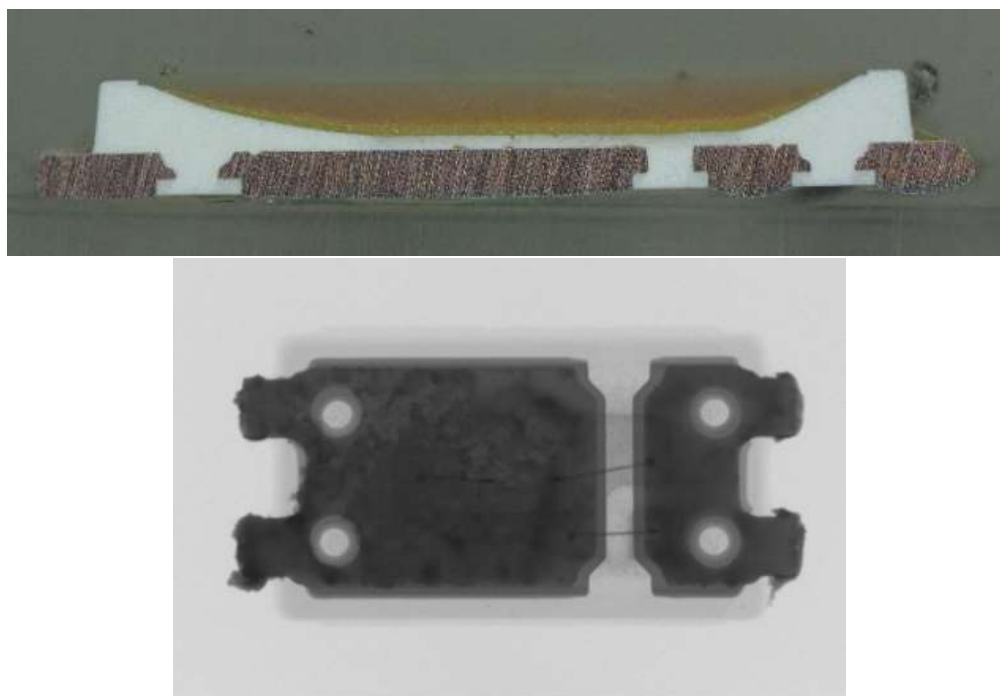
INFRINGEMENT OF U.S. PATENT NO. 10,134,967

EXEMPLARY CLAIM 1

1
2 129. The Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED
3 includes a plurality of LED packages. Optical microscope images of an LED package from the
4 Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED are reproduced below
5 before and after removal of an encapsulant. An LED chip is disposed on the top surface of the right
6 lead frame in the optical image.



11 130. The top image below is an optical microscope image of a cross-section through the
12 LED package. The bottom image below is an x-ray through the package.



24 131. As the above images show, the LED package contains two metal lead frames spaced
25 apart from each other. The optical image shows that each lead frame has a substantially flat top
26 surface, a bottom surface, a fixing hole, and sidewalls between the top and bottom surfaces.

27 132. The cross-section image above depicts the cross-sectional shape of the sidewalls of
28

1 both lead frames at the sides that face each other in the horizontal direction. In addition, the upper
2 surface of the left and right lead frames extend further into the space between the lead frames than
3 the bottom surface of the left and right lead frames. That the top surfaces extend further into the
4 space indicates an undercut in the sidewall that partially defines a fixing space between the two
5 lead frames. This same feature is also depicted in the x-ray image above. In particular, both leads
6 are shown with relatively dark interior regions and three sides have relatively light outer regions.
7 The differences in brightness correlate to the thickness of the metal at those locations. The relatively
8 light outer regions correspond to undercut sidewalls on three sides of both lead frames. The
9 undercuts form the fixing space.

10 133. As discussed above and as shown in the images above, the first and second lead
11 frames include a fixing hole located in the interior portions and each fixing hole includes an
12 undercut sidewall that envelopes inner bounds of the fixing hole.

13 134. As discussed above, the lead frames face each other in the horizontal direction and
14 both lead frames have three undercut sidewalls as indicated by the cross-sectional and x-ray images
15 above. Of those three undercut sidewalls, each lead frame has a sidewall shown as extending
16 vertically in the x-ray image and two sidewalls shown as extending horizontally. The sidewalls
17 extending horizontally in the x-ray image are all parallel and also perpendicular to those shown
18 extending vertically.

19 135. Finelite's infringement has caused and is continuing to cause damage and
20 irreparable injury to Plaintiffs. Plaintiffs will continue to suffer damage and irreparable injury
21 unless and until that infringement is enjoined by this Court, as a remedy at law alone would be
22 inadequate.

23 136. Plaintiffs are entitled to injunctive relief and damages in accordance with 35 U.S.C.
24 §§ 271, 281, 283, and 284.

25 **COUNT 14**

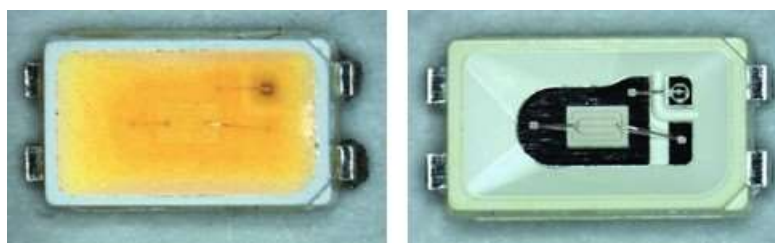
26 **INFRINGEMENT OF U.S. PATENT NO. 10,510,933**

27 **EXEMPLARY CLAIM 15**

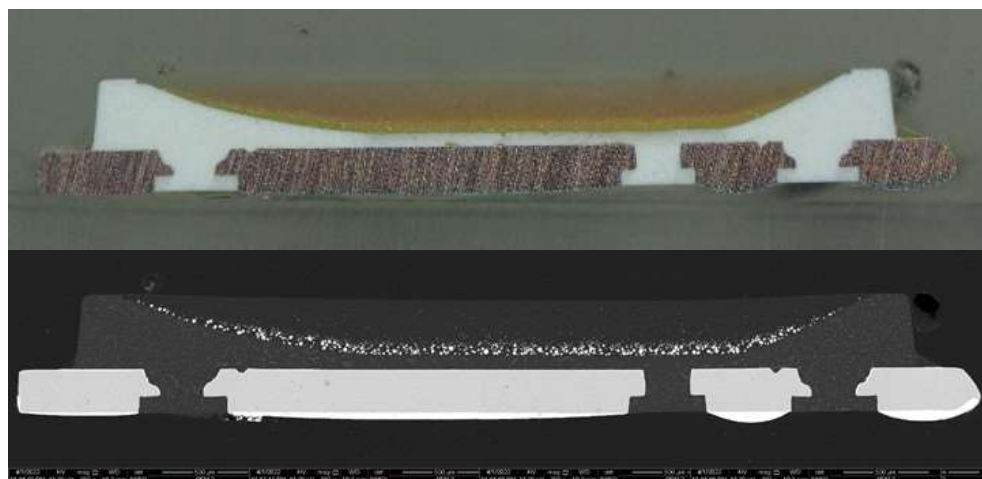
28 137. Finelite has infringed and continues to infringe one or more claims of the '933

1 patent, including but not limited to exemplary claim 15, in violation of 35 U.S.C. § 271(a), at least
 2 by without authority making, using, offering to sell and/or selling the Finelite HPR-A-2X2-S-835-
 3 DCO-LED-3500K-277V-SC-C1-OBO LED panel within the United States.

4 138. The Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED panel
 5 includes a plurality of LED packages, each of which includes a light emitting diode. The image of
 6 an LED package from the Finelite HPR-A-2X2-S-835-DCO-LED-3500K-277V-SC-C1-OBO LED
 7 panel is reproduced below left. The image below right shows an LED chip within package. The
 8 chip emits blue light with a Full Width at Half Maximum of less than 40 nm.



13 139. The image above left is an optical microscope image of the top surface of the
 14 package, where the white material comprises a housing. The images below are an optical
 15 microscope image (top) and a SEM image (bottom) created after cross-sectioning the package. The
 16 housing material appears white in the optical image. The image shows that the package includes a
 17 housing having both a top surface, an opposite bottom surface, wherein the top surface includes,
 18 from top to bottom, upper, intermediate, and lower portions.



27 140. Also shown in the image above is an encapsulating molding part formed over and
 28 around the light-emitting diode chip. The molding part is a silicon based encapsulant. The relatively

1 bright inclusions within the encapsulant include two different phosphors, one a Lutetium-based
2 phosphor that has an output wavelength in the green with a peak between 520 nm and 570 nm, and
3 the other a nitride-based phosphor that has an output wavelength in the red with a peak between
4 600 nm and 670 nm. The Full Width at Half Maximum of the nitride-based phosphor is narrower
5 than that of the Lutetium-based phosphor, which is wider than 40 nm.

6 141. The output from the blue chip combined with light down converted by the first and
7 second phosphors is white light.

8 142. Finelite's infringement has caused and is continuing to cause damage and
9 irreparable injury to Plaintiffs. Plaintiffs will continue to suffer damage and irreparable injury
10 unless and until that infringement is enjoined by this Court, as a remedy at law alone would be
11 inadequate.

12 143. Plaintiffs are entitled to injunctive relief and damages in accordance with 35 U.S.C.
13 §§ 271, 281, 283, and 284.

14
15 **PRAYER FOR RELIEF**

16 WHEREFORE, the Plaintiffs requests that the Court enter judgment in their favor and
17 against Defendant Finelite, Inc., as follows:

18 A. A judgment that Defendant infringe the '069, '868, '800, '225, '638, '210, '514,
19 '496, '207, '952, '410, '157, '967 and '933 patents;

20 B. A preliminary and permanent injunction restraining and enjoining Defendant, its
21 officers, partners, agents, servants, employees, parents, subsidiaries, divisions, affiliate
22 corporations, joint ventures, other related business entities and all other persons acting in concert,
23 participation, or in privity with them, and their successors and assigns, from infringing the '069,
24 '868, '800, '225, '638, '210, '514, '496, '207, '952, '410, '157, '967 and '933 patents;

25 C. An award of damages to Plaintiffs Seoul Semiconductor and Seoul Viosys arising
26 from Defendant's past and continuing infringement up until the date Defendant is finally and
27 permanently enjoined from further infringement, including compensatory damages;
28

1 D. A determination that Defendant's infringement of one or more of the '210, '514,
2 and '157 patents was willful, and a trebling of damages pursuant to 35 U.S.C. § 284;

3 E. A determination that this is an exceptional case and awarding the Seoul Plaintiffs'
4 attorneys' fees pursuant to 35 U.S.C. § 285; An order awarding the Seoul Plaintiffs the costs and
5 expenses that they have incurred in prosecuting this action;

6 F. An order awarding the Seoul Plaintiffs pre- and post-judgment interest on their
7 damages; and

8 G. Such other and further relief in law or in equity as this Court deems just and proper.
9

10 **JURY DEMAND**

11 Plaintiffs Seoul Semiconductor and Seoul Viosys respectfully request a jury trial
12 on all issues so triable.

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Dated: May 13, 2022

Respectfully submitted,

STEPTOE & JOHNSON LLP

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